

# An Indexed Model of Impredicative Polymorphism and Mutable References

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## Abstract

*We present a semantic model of the polymorphic lambda calculus augmented with a higher-order store, allowing the storage of values of any type, including impredicative quantified types, mutable references, recursive types, and functions. Our model provides the first denotational semantics for a type system with updatable references to values of impredicative quantified types. The central idea behind our semantics is that instead of tracking the exact type of a mutable reference in a possible world our model keeps track of the approximate type. While high-level languages like ML and Java do not themselves support storage of impredicative existential packages in mutable cells, this feature is essential when representing ML function closures, that is, in a target language for typed closure conversion of ML programs.*

## 1 Introduction

Semantics of mutable references are hard [11, 17]. Recent possible-worlds models [1, 13] work well, but do not permit polymorphism. Semantics of impredicative polymorphism are also hard [9] and, as far as we know, there are no possible-worlds models of impredicative polymorphism. Combining mutable references with polymorphism can be extremely tricky. Our LICS'02 paper [2] showed how to model mutable references, and we claimed that we could handle impredicative polymorphism. We discovered, however, that we could mix them in all ways except for one: values of quantified types could not be stored in a mutable cell. But this mixture of features is necessary for typed closure conversion (type-preserving compilation of higher-order functional programming languages) [14], even without intensional type-passing [15]. There may, of course, be other, as yet undiscovered, ways of representing function closures, but for now it seems we need mutable references to impredicative quantified types.

For our Foundational Proof-Carrying Code project [3], we wish to construct type systems with soundness proofs

that are machine-checkable in the simplest possible logic. Conventional syntactic type systems have proofs that are syntactic metatheorems by induction over proofs; to make such a proof machine-checkable requires a complicated and sophisticated checker such as the metatheory engine of the Twelf system [16]. Crary [10] has built a syntactic progress-and-preservation proof in the style of Harper [12] for a typed assembly language with mutable fields and impredicative polymorphism — the proof is checked by the Twelf metatheorem prover. In contrast, our semantic approach has been to define a type as a predicate on data structures, and a type constructor as a transformer of these predicates [4, 5, 2]. (In practice, a type can't be quite as simple as a predicate on terms; there must be auxiliary parameters that encode, in effect, a semantic domain construction.) Then each typing rule can be proved as a derived lemma in higher-order logic; these proofs are machine-checkable by a very simple and trustworthy program indeed [6]. The goal is to prove and use a type-soundness theorem: a given machine-language program type-checks; type-checking implies safety; so the program is safe.

Our prototype compiler [8] translates Core ML into Sparc machine language. The type-preserving translation of higher-order ML functions into function closures uses an existential quantifier to hide the type of the function environment [14]. This environment can contain other function-closures—that is, the instantiation of the existential can be by other existential types—so that the quantification must be impredicative. Of course, ML has mutable references, and a function-closure can be stored into a mutable reference, so references must be able to contain existentials and vice versa. Therefore we needed to construct the semantic model that we will describe in this paper, with all of these features as well as other features that we have previously described how to model: recursive types [5], heap allocation of data structures [4], address arithmetic, and so on.

In this paper, to keep the presentation simple, we will show only a model for  $\lambda$ -calculus. The machine-checked proof we are constructing (in higher-order logic represented in LF and type-checked by Twelf) is embedded in our pro-

totype PCC system for von Neumann machines (e.g., the Sparc); Appel and McAllester [5] show the similarities between models of types for  $\lambda$ -calculus and for von Neumann machines.

Our model, and proofs, follow as closely as possible the Appel and McAllester schema, so we will summarize it here. In that model, a type  $\tau$  is a set of pairs  $\langle k, v \rangle$ , where  $k$  is a natural number and  $v$  is a ( $\lambda$ -calculus) value. The intuitive idea is that in any computation running for no more than  $k$  steps, the value  $v$  behaves as if it were an element of the type  $\tau$ . A typing judgment  $e :_k \tau$  on a closed expression  $e$  means (by definition) that  $e$  can't get stuck within  $k$  steps, and furthermore if  $e$  reduces in  $j < k$  steps to a value  $v$  then  $\langle k - j, v \rangle \in \tau$ . They call  $k$  the *approximation index* in such judgments.

Then they give a semantic definition of type-checking open expressions in a context,  $\Gamma \models_k e : \tau$ , and finally define  $\Gamma \models e : \tau$  to mean  $\forall k \geq 0. (\Gamma \models_k e : \tau)$ . Using these definitions, they can prove conventional-looking rules such as

$$\frac{\Gamma \models e_1 : \tau_1 \rightarrow \tau_2 \quad \Gamma \models e : \tau_1}{\Gamma \models (e_1 e_2) : \tau_2}$$

directly from the definitions. Even the proofs regarding fold/unfold of contravariant equirecursive types are remarkably concise.

When we add mutable references, the small-step operational semantics is no longer of the form  $e \mapsto e'$ , but is  $(M, e) \mapsto (M', e')$ . In our new semantics, an approximate typing judgment is no longer just  $v :_k \tau$  but  $v :_{k, \Psi, M} \tau$ . That is, to judge whether a value  $v$  is well typed, we must have a memory  $M$  in which to look up any location-labels that might appear within  $v$ , and we also need a *memory typing*  $\Psi$  that tells us about the types of those locations. In our new model, therefore, a type  $\tau$  is a set of four-tuples  $\langle k, \Psi, M, v \rangle$ .

A most delicate part of the semantic construction is the fact that a type is a set of four-tuples, and the  $\Psi$  component of one of those four-tuples is itself a mapping from locations to types. If the construction were attempted naively, this would lead to a cardinality paradox. We avoid all paradoxes as follows. First, given  $\langle k, \Psi, M, v \rangle$  and a location  $\ell$  such that  $\Psi(\ell) = \tau'$ , we make sure that every element  $\langle k', \Psi', M', v' \rangle \in \tau'$  has  $k' < k$ . Because of this inherent wellfoundedness, we can use a version of our previously described stratified Gödelization [2] to embed the entire model into ordinary higher-order logic.

Our previous stratified model of mutable references equipped each machine state  $(M, e)$  with a memory typing  $\Psi$  (called *allocset*  $a$  in that paper) that mapped each mutable location to its type. To avoid circularity, we had to ensure that all the  $\Psi$ 's in the semantic definition of a type  $\tau$  contained only strictly less complex types than  $\tau$ . The third level in our hierarchy of types contained types such as  $\text{ref}(\text{ref}(\text{int}))$  but not  $\text{ref}(\text{ref}(\text{ref}(\text{int})))$ . For any (fi-

nite) type expression there was some level of the hierarchy powerful enough to contain it. Since all the type expressions in a well-typed monomorphic program are finite, we could find some level of the hierarchy strong enough to type each program.

However, in the presence of quantified types we encountered a problem. Consider, for example, the type  $\exists \alpha. \text{ref}(\alpha \times \alpha)$ . We cannot predict how complex will be the type that instantiates  $\alpha$ , so there is no finite level of the hierarchy that is guaranteed powerful enough. For example, if  $\alpha$  is instantiated with  $\tau = \text{ref}^{50}(\text{int})$ , then the 43rd level of the hierarchy won't contain  $\tau$ —only the 51st level (and above) will.

The solution, which we describe more rigorously in the next sections, is to use the approximation power of the indexed model. Suppose in some execution we are about to instantiate  $\exists \alpha. \text{ref}(\alpha \times \alpha)$  with  $\text{ref}^{50}(\text{int})$ , but we intend to run the program for only 30 more execution steps. Then it's all the same whether we instantiate with  $\text{ref}^{50}(\text{int})$  or with  $\text{ref}^{30}(\perp)$ , since in 30 execution steps the program cannot dereference more than 30 references. Therefore, if we intend to run the program for only  $k$  steps, it suffices to use the  $k$ th level of the hierarchy.

## 2 Indexed Types for the Lambda Calculus

**Syntax.** The language we shall consider is the polymorphic lambda calculus augmented with mutable references, existential and recursive types, and the constant  $\mathbf{0}$ . Cartesian products and other standard constructions are easy to add to the model we will present. The syntax of lambda terms is given by the following grammar.

$$\begin{array}{ll} \text{Expressions } e & ::= x \mid \ell \mid \mathbf{0} \mid \lambda x. e \mid (e_1 e_2) \mid \\ & \quad \text{new}(e) \mid !e \mid e_1 := e_2 \mid \Lambda. e \mid e[] \mid \\ & \quad \text{pack } e \mid \text{open } e_1 \text{ as } x \text{ in } e_2 \\ \text{Values } v & ::= \ell \mid \mathbf{0} \mid \lambda x. e \mid \Lambda. e \mid \text{pack } v \end{array}$$

We use the meta-variable  $x$  to range over a countably infinite set of *variables* and the meta-variable  $\ell$  to range over a countably infinite set of *locations*. A term  $v$  is a *value* if it is a location  $\ell$ , the constant  $\mathbf{0}$ , a term or type abstraction, or an existential package, and if it contains no free term variables  $x$ .

This syntax is slightly unconventional: in most presentations the polymorphic operators  $\Lambda \alpha. e$  and  $e[\tau]$  and the existential operators  $\text{pack}$  and  $\text{open}$  mention types syntactically. We wish to give purely semantic (not syntactic) typings to untyped lambda calculus, so we omit all types from our syntax. We let the vestigial operators remain in the untyped syntax to simplify the presentation.

**Operational Semantics and Safety.** A *memory*  $M$  is a mapping from locations to closed values. The small-step semantics (see Figure 1) is given by an abstract machine. The

$$\begin{array}{c}
\frac{(M, e_1) \mapsto (M', e'_1)}{(M, e_1 e_2) \mapsto (M', e'_1 e'_2)} \quad \frac{(M, e_2) \mapsto (M', e'_2)}{(M, (\lambda x.e_1) e_2) \mapsto (M', (\lambda x.e_1) e'_2)} \quad \frac{}{(M, (\lambda x.e) v) \mapsto (M, e[v/x])} \\
\frac{(M, e) \mapsto (M', e')}{(M, \mathbf{new}(e)) \mapsto (M', \mathbf{new}(e'))} \quad \frac{\ell \notin \text{dom}(M)}{(M, \mathbf{new}(v)) \mapsto (M[\ell := v], \ell)} \quad \frac{(M, e) \mapsto (M', e')}{(M, !e) \mapsto (M', !e')} \quad \frac{\ell \in \text{dom}(M)}{(M, !\ell) \mapsto (M, M(\ell))} \\
\frac{(M, e_1) \mapsto (M', e'_1)}{(M, e_1 := e_2) \mapsto (M', e'_1 := e'_2)} \quad \frac{(M, e_2) \mapsto (M', e'_2)}{(M, v_1 := e_2) \mapsto (M', v_1 := e'_2)} \quad \frac{\ell \in \text{dom}(M)}{(M, \ell := v) \mapsto (M[\ell := v], \mathbf{0})} \\
\frac{(M, e) \mapsto (M', e')}{(M, e[]) \mapsto (M', e'[])} \quad \frac{}{(M, (\Lambda.e)[]) \mapsto (M, e)} \quad \frac{(M, e) \mapsto (M', e')}{(M, \mathbf{pack} e) \mapsto (M', \mathbf{pack} e')} \\
\frac{(M, e_1) \mapsto (M', e'_1)}{(M, \mathbf{open} e_1 \text{ as } x \text{ in } e_2) \mapsto (M', \mathbf{open} e'_1 \text{ as } x \text{ in } e_2)} \quad \frac{}{(M, \mathbf{open} (\mathbf{pack} v) \text{ as } x \text{ in } e_2) \mapsto (M, e_2[v/x])}
\end{array}$$

Figure 1. Small-step operational semantics

state of the abstract machine is described by a pair  $(M, e)$  of a memory and an expression. We write  $(M, e) \mapsto^j (M', e')$  to mean that there exists a chain of  $j$  steps of the form  $(M, e) \mapsto (M_1, e_1) \mapsto \dots \mapsto (M_j, e_j)$  where  $M_j$  is  $M'$  and  $e_j$  is  $e'$ . We write  $(M, e) \mapsto^* (M', e')$  if  $(M, e) \mapsto^j (M', e')$  for some  $j \geq 0$ . A state  $(M, e)$  is *irreducible* if it has no successor in the step relation, that is  $\text{irred}(M, e)$  if  $e$  is a value or  $(M, e)$  is a “stuck” state such as  $(M, \mathbf{0}(e'))$  or  $(M, !\ell)$  where  $\ell \notin \text{dom}(M)$ .

We say that  $(M, e)$  is safe for  $k$  steps if for any reduction  $(M, e) \mapsto^j (M', e')$  of  $j < k$  steps, either  $e'$  is a value or  $(M', e') \mapsto (M'', e'')$ . Note that any state is safe for 0 steps. A state  $(M, e)$  is called safe if it is safe for all  $k \geq 0$ .

**Semantics of Types and Typing Rules.** We are interested in constructing methods for proving that a given state is safe. In particular, we want to prove the rules (lemmas) of Figure 2 and also that typability implies safety. For simplicity in this presentation, we avoid the use of type variables; instead of writing  $\exists \alpha. \tau$  with type variables  $\alpha$  in a type expression  $\tau$ , we write  $\exists F$  where  $F$  is a function from types to types. In this paper we assume  $F$  is somehow expressible in the underlying logic, but Appel, Richards, and Swadi [7] show how best to handle type expressions and variables in our semantic approach.

The semantic approach taken here is based on types as sets rather than type expressions. We say that a *type* is a set  $\tau$  of tuples of the form  $\langle k, \Psi, M, v \rangle$  where  $k$  is a non-negative integer,  $\Psi$  is a *memory typing*, that is, a mapping from locations to closed types,  $M$  is a memory, and  $v$  is a value. Informally,  $\langle k, \Psi, M, v \rangle \in \tau$  means that  $v$  “looks” like it belongs to type  $\tau$ ; perhaps  $v$  is not “really” a member of type  $\tau$ , but any program of type  $\tau \rightarrow \tau'$  must execute for at least  $k$  steps on  $v$  before getting to a stuck state.

As mentioned in the previous section, a naive construction of  $\tau$  as a set of  $\langle k, \Psi, M, v \rangle$ , with  $\Psi$  as a relation on

$$\begin{array}{c}
\frac{}{\Gamma \models x : \Gamma(x)} \text{ (var)} \quad \frac{}{\Gamma \models \mathbf{0} : \mathbf{unit}} \text{ (unit)} \\
\frac{\Gamma[x := \tau_1] \models e : \tau_2}{\Gamma \models \lambda x.e : \tau_1 \rightarrow \tau_2} \text{ (abs)} \\
\frac{\Gamma \models e_1 : \tau_1 \rightarrow \tau_2 \quad \Gamma \models e_2 : \tau_1}{\Gamma \models (e_1 e_2) : \tau_2} \text{ (app)} \\
\frac{\Gamma \models e : \mu F}{\Gamma \models e : F(\mu F)} \text{ (unfold)} \quad \frac{\Gamma \models e : F(\mu F)}{\Gamma \models e : \mu F} \text{ (fold)} \\
\frac{\Gamma \models e : \tau}{\Gamma \models \mathbf{new}(e) : \mathbf{ref} \tau} \text{ (new)} \quad \frac{\Gamma \models e : \mathbf{ref} \tau}{\Gamma \models !e : \tau} \text{ (deref)} \\
\frac{\Gamma \models e_1 : \mathbf{ref} \tau_2 \quad \Gamma \models e_2 : \tau_2}{\Gamma \models e_1 := e_2 : \mathbf{unit}} \text{ (assign)} \\
\frac{\forall \tau. \text{type}(\tau) \Rightarrow \Gamma \models e : F(\tau)}{\Gamma \models \Lambda.e : \forall F} \text{ (tabs)} \\
\frac{\text{type}(\tau) \quad \Gamma \models e : \forall F}{\Gamma \models e[] : F(\tau)} \text{ (tapp)} \\
\frac{\text{type}(\tau) \quad \Gamma \models e : F(\tau)}{\Gamma \models \mathbf{pack} e : \exists F} \text{ (pack)} \\
\frac{\Gamma \models e_1 : \exists F \quad \forall \tau. \text{type}(\tau) \Rightarrow \Gamma[x := F(\tau)] \models e_2 : \tau_2}{\Gamma \models \mathbf{open} e_1 \text{ as } x \text{ in } e_2 : \tau_2} \text{ (open)}
\end{array}$$

Figure 2. Type-checking lemmas

$\ell$  and  $\tau$ , can lead to paradoxes. That is, there is the question of exactly what logic we are using, and what are the metalogical types of  $\tau$  and  $\Psi$ . We will explain in Section 5 how to represent our proof in the Calculus of Inductive Constructions, and in Section 6 how we formulate a solution in higher-order logic using Gödelization.

A type is not just any set of tuples  $\langle k, \Psi, M, v \rangle$ ; it must be well behaved in certain ways. To formally define a type

we must first provide some auxiliary definitions.

**Definition 1 (Approx)**

The  $k$ -approximation of a set is the subset of its elements whose index is less than  $k$ ; we also extend this notion pointwise to memory typings:

$$\begin{aligned} \lfloor \tau \rfloor_k &\equiv \{ \langle j, \Psi, M, v \rangle \mid j < k \wedge \langle j, \Psi, M, v \rangle \in \tau \} \\ \lfloor \Psi \rfloor_k &\equiv \{ \langle \ell \mapsto \lfloor \tau \rfloor_k \mid \Psi(\ell) = \tau \} \end{aligned}$$

Intuitively, if we intend to run the program for no more than  $k$  more steps, then typechecking with  $\lfloor \tau \rfloor_k$  is just as safe as typechecking with  $\tau$ .

**Definition 2 (Well-typed Memory)**

A memory  $M$  is well-typed to approximation  $k$  with respect to a memory typing  $\Psi$  iff  $\text{dom}(\Psi) \subseteq \text{dom}(M)$ , the domain of  $M$  is finite, and the contents of each location  $\ell \in \text{dom}(\Psi)$  has type  $\Psi(\ell)$  to approximation  $k$ :

$$M :_k \Psi \equiv \text{dom}(\Psi) \subseteq \text{dom}(M) \wedge \text{finite}(\text{dom}(M)) \wedge \forall j < k. \forall \ell \in \text{dom}(\Psi). \langle j, \lfloor \Psi \rfloor_j, M, M(\ell) \rangle \in \lfloor \Psi \rfloor_k(\ell)$$

The domain of  $M$  includes all locations already allocated; elements not in  $\text{dom}(M)$  are available to be allocated by new in future computation steps. We permit the memory to have “extra” allocated locations that are not typed by  $\Psi$ , but the model would also work if we required  $\text{dom}(\Psi) = \text{dom}(M)$ . Also note that all the tuples required to be in  $\Psi(\ell)$  have index strictly less than  $k$ ; this helps avoid circularity.

**Definition 3 (Memory Extension)**

A valid memory extension is defined as follows:

$$\begin{aligned} \langle k, \Psi, M \rangle \sqsubseteq \langle j, \Psi', M' \rangle &\equiv \\ j \leq k \wedge (\forall \ell \in \text{dom}(\Psi). \lfloor \Psi' \rfloor_j(\ell) = \lfloor \Psi \rfloor_j(\ell)) \wedge M' :_j \Psi' & \end{aligned}$$

Memory extension models what may happen to the memory (and memory typing) during zero or more computation steps. The computation step(s) might allocate a new reference, in which case  $\text{dom}(\Psi')$  will be a strict superset of  $\text{dom}(\Psi)$ . The step(s) might choose to forget some information, in which case  $j$  will be strictly less than  $k$  and  $\Psi'$  may be more approximate than  $\Psi$ —but in this case, the program will now be able to run for at most  $j$  more steps instead of  $k$  more steps. The computation might store a new value at some location  $\ell$ , but in this case the new value must (approximately) obey the typing given by  $\Psi'(\ell)$ .

**Definition 4 (Type)**

A type is a set  $\tau$  of tuples of the form  $\langle k, \Psi, M, v \rangle$  where  $v$  is a value,  $k$  is a nonnegative integer,  $\Psi$  is a memory typing, and  $M$  is a memory such that  $M :_k \Psi$ , and where the set  $\tau$  is such that if  $\langle k, \Psi, M, v \rangle \in \tau$  and  $\langle k, \Psi, M \rangle \sqsubseteq \langle j, \Psi', M' \rangle$  then  $\langle j, \Psi', M', v \rangle \in \tau$ .

$$\begin{aligned} \perp &\equiv \{ \} \\ \mathbf{unit} &\equiv \{ \langle k, \Psi, M, \mathbf{0} \rangle \mid M :_k \Psi \} \\ \tau_1 \rightarrow \tau_2 &\equiv \{ \langle k, \Psi, M, \lambda x. e \rangle \mid M :_k \Psi \wedge \\ &\quad \forall j < k. \forall v, \Psi', M'. \\ &\quad \quad (\langle k, \Psi, M \rangle \sqsubseteq \langle j, \Psi', M' \rangle \wedge \langle j, \Psi', M', v \rangle \in \tau_1) \\ &\quad \Rightarrow e[v/x] :_{j, \Psi', M'} \tau_2 \} \\ \mu F &\equiv \{ \langle k, \Psi, M, v \rangle \mid \langle k, \Psi, M, v \rangle \in F^{k+1}(\perp) \} \\ \mathbf{ref} \tau &\equiv \{ \langle k, \Psi, M, \ell \rangle \mid M :_k \Psi \wedge \lfloor \Psi \rfloor_k(\ell) = \lfloor \tau \rfloor_k \wedge \\ &\quad \forall j < k. \langle j, \lfloor \Psi \rfloor_j, M, M(\ell) \rangle \in \tau \} \\ \forall F &\equiv \{ \langle k, \Psi, M, \Lambda. e \rangle \mid M :_k \Psi \wedge \\ &\quad \forall j, \Psi', M', \tau. (\langle k, \Psi, M \rangle \sqsubseteq \langle j, \Psi', M' \rangle \wedge \text{type}(\lfloor \tau \rfloor_j)) \\ &\quad \Rightarrow \forall i < j. e :_{i, \lfloor \Psi' \rfloor_i, M'} F(\tau) \} \\ \exists F &\equiv \{ \langle k, \Psi, M, \text{pack } v \rangle \mid M :_k \Psi \wedge \\ &\quad \exists \tau. \text{type}(\lfloor \tau \rfloor_k) \wedge \forall j < k. \langle j, \lfloor \Psi \rfloor_j, M, v \rangle \in F(\tau) \} \end{aligned}$$

Figure 3. Type definitions

The essential property of a type is that it is closed under memory extension. This will allow us to prove that if  $M(\ell)$  has type  $\tau$ , and  $(M, e)$  steps by computation to  $(M', e')$ , then  $M(\ell)$  still has type  $\tau$ , even if other locations in  $M$  are stored into.

**Definition 5 (Expr : Type)**

For any closed expression  $e$  and type  $\tau$  we write  $e :_{k, \Psi, M} \tau$  if whenever  $(M, e) \mapsto^j (M', e')$  for  $j < k$  and  $(M', e')$  irreducible, then there exists a memory typing  $\Psi'$  such that  $\langle k, \Psi, M \rangle \sqsubseteq \langle k - j, \Psi', M' \rangle$  and  $\langle k - j, \Psi', M', e' \rangle \in \tau$ ; that is,

$$\begin{aligned} e :_{k, \Psi, M} \tau &\equiv \forall j, M', e'. (0 \leq j < k \wedge (M, e) \mapsto^j (M', e') \\ &\quad \wedge \text{irred}(M', e')) \\ &\quad \Rightarrow \exists \Psi'. \langle k, \Psi, M \rangle \sqsubseteq \langle k - j, \Psi', M' \rangle \\ &\quad \wedge \langle k - j, \Psi', M', e' \rangle \in \tau \end{aligned}$$

Intuitively,  $e :_{k, \Psi, M} \tau$  means that in a state  $(M, e)$ ,  $e$  behaves like an element of  $\tau$  for  $k$  steps of computation. Note that if  $e :_{k, \Psi, M} \tau$  and  $0 \leq j \leq k$  then  $e :_{j, \Psi', M} \tau$ , for an appropriate  $\Psi'$ , by the fact that  $\tau$  is closed under memory extension. Also, for a value  $v$ , and  $k > 0$ , the statements  $v :_{k, \Psi, M} \tau$  and  $\langle k, \Psi, M, v \rangle \in \tau$  are equivalent.

We now define the types and type constructors of our language as sets and functions from sets to sets. The definitions appear in Figure 3. The idea (as in the Appel-McAllester indexed model) is that for a value  $\lambda x. e$  in  $\tau_1 \rightarrow \tau_2$  to be safe for  $k$  steps, it must be that if we use up one step by beta-reduction, the resulting expression must be safe for  $j < k$  steps.

What’s new here is the definition of the quantified types. For a value  $\Lambda. e$  to belong (with approximation  $k$ ) to  $\forall F$ , we can use only the  $i$ th level of the memory-typing  $\Psi$ , for  $i$  strictly less than  $k$ . This approximation is justified, because then we are careful to use only the approximate typing judgment that  $e$  is in the  $i$ th approximation of  $F(\tau)$ .

Other types, such as cartesian products, integers, unions, intersections, and so on, are straightforward to define in this manner but we omit them to simplify the presentation.

**Open expressions.** Up to now we have dealt with closed expressions, as these are the ones that “step” at “run time.” Now we turn to expressions with free variables, upon which the static type-checking rules must operate.

**Definition 6 (Semantics of Judgment)**

A context is a mapping from lambda calculus variables to types. A substitution is a mapping from lambda calculus variables to values. For any context  $\Gamma$  and substitution  $\sigma$  we write  $\sigma :_{k, \Psi, M} \Gamma$  (“ $\sigma$  approximately obeys  $\Gamma$ ”) if for all variables  $x \in \text{dom}(\Gamma)$  we have  $\sigma(x) :_{k, \Psi, M} \Gamma(x)$ .

Finally, we write  $\Gamma \models_k e : \tau$  to mean that every free variable of  $e$  is mapped by  $\Gamma$  and

$$\forall \sigma, \Psi, M. (M :_k \Psi \wedge \sigma :_{k, \Psi, M} \Gamma) \Rightarrow \sigma(e) :_{k, \Psi, M} \tau$$

where  $\sigma(e)$  is the result of replacing the free variables in  $e$  with their values under  $\sigma$ .

That is, the meaning of the judgment  $\Gamma \models_k e : \tau$  on an open expression  $e$  and type  $\tau$  can be obtained from our semantics of a similar judgment on closed expressions, so long as we quantify over all (approximately) legitimate substitutions of values for variables.

**Definition 7**

We write  $\Gamma \models e : \tau$  if for all  $k \geq 0$  we have  $\Gamma \models_k e : \tau$ . We write  $\models e : \tau$  to mean  $\Gamma_0 \models e : \tau$  for the empty context  $\Gamma_0$ .

Note that  $\Gamma \models e : \tau$  can be viewed as a three place relation that holds on the context  $\Gamma$ , the term  $e$ , and the type  $\tau$ . Each of the type inference lemmas in Figure 2 states that if certain instances of the relation  $\Gamma \models e : \tau$  hold, then certain other instances hold. Once we have proved the type inference lemmas in Figure 2, these lemmas can be used in the same manner as standard type inference rules to prove statements of the form  $\Gamma \models e : \tau$ . We now observe that the definitions given above imply the following.

**Theorem 8**

If  $\models e : \tau$ ,  $\tau$  is a type, and  $M$  is a finite memory then  $(M, e)$  is safe.

**Proof:** In a conventional syntactic type theory, the safety theorem (typability implies safety) is difficult (or at least tedious) to prove. Here it follows directly from the definitions. We need to show that for any  $k$ , it is safe to execute  $(M, e)$  for  $k$  steps. From  $\models e : \tau$  we have  $\Gamma_0 \models_k e : \tau$ , and therefore  $e$  is closed (since  $\Gamma_0$  is empty). Choose the empty substitution  $\sigma_0$  and the empty memory typing  $\Psi_0$ , and by the definition of  $\models_k$  we have

$$M :_k \Psi_0 \wedge \sigma_0 :_{k, \Psi_0, M} \Gamma_0 \Rightarrow \sigma_0(e) :_{k, \Psi_0, M} \tau$$

The two premises are trivially satisfied; applying the trivial substitution we obtain  $e :_{k, \Psi_0, M} \tau$ . By definition, this

means that if  $(M, e)$  steps in fewer than  $k$  steps to  $(M', e')$ , then either  $e'$  is a value or another step is possible.  $\square$

A “program” is a closed expression that does not contain any location symbols  $\ell$ . When a program begins executing, it steps (by means of `new`) to expressions that may contain location symbols. A conventional subject-reduction proof requires the static type system to be able to type-check programs during execution, so there must be a way to type-check locations  $\ell$ . However, our judgment  $\Gamma \models e : \tau$  has no provision to type-check labels (e.g., there is no label-environment to the left of the  $\models$  symbol). We don’t need to type-check executing programs (since we’re not doing subject reduction), and so we don’t need to type-check location symbols. This is one reason that, in the proof of Theorem 8, we are able to choose the empty memory-typing  $\Psi_0$ .

### 3 Proofs of Types

In order to prove that a program  $e$  with type  $\tau$  is safe to execute (Theorem 8), it must be the case that  $\tau$  is a type. Next, we prove that each of the type constructors shown in Figure 3 is a type, or produces a type when applied to valid arguments. The fact that  $\perp$  and `unit` are types follows immediately from their definitions. To prove that  $\tau_1 \rightarrow \tau_2$  is a type, we need the following lemma which says that memory extension is transitive. The proof is given in Appendix A.

**Lemma 9 (Memory Extension Transitive)**

If  $(k_1, \Psi_1, M_1) \sqsubseteq (k_2, \Psi_2, M_2)$  and  $(k_2, \Psi_2, M_2) \sqsubseteq (k_3, \Psi_3, M_3)$  then  $(k_1, \Psi_1, M_1) \sqsubseteq (k_3, \Psi_3, M_3)$ .

**Lemma 10 (Type  $\tau_1 \rightarrow \tau_2$ )**

If  $\tau_1$  and  $\tau_2$  are types then  $\tau_1 \rightarrow \tau_2$  is also a type.

**Proof:** First, if  $\langle k, \Psi, M, v \rangle \in \tau_1 \rightarrow \tau_2$  then  $M :_k \Psi$ . This is immediate from the definition of  $\rightarrow$ .

Next, we must prove that  $\tau_1 \rightarrow \tau_2$  is closed under valid memory extension. Suppose that  $\langle k, \Psi, M, v \rangle \in \tau_1 \rightarrow \tau_2$  and  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$ . Note that  $\langle k, \Psi, M, v \rangle \in \tau_1 \rightarrow \tau_2$  implies that  $v$  is of the form  $\lambda x.e$ . We must prove that  $\langle j, \Psi', M', \lambda x.e \rangle \in \tau_1 \rightarrow \tau_2$ . We first require that  $M' :_j \Psi'$  which immediately follows from  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$ . Next, let  $(j, \Psi', M') \sqsubseteq (i, \Psi'', M'')$  and  $\langle i, \Psi'', M'', v'' \rangle \in \tau_1$  for  $i < j$  and some value  $v''$  — we have to show  $e[v''/x] :_{i, \Psi'', M''} \tau_2$ . Since  $i < j$  and, by the definition of  $\sqsubseteq$ , we have  $j \leq k$ , it follows that  $i < k$ ; by Lemma 9 we have  $(k, \Psi, M) \sqsubseteq (i, \Psi'', M'')$ ; and we already have  $\langle i, \Psi'', M'', v'' \rangle \in \tau_1$ . These three statements together with  $\langle k, \Psi, M, \lambda x.e \rangle \in \tau_1 \rightarrow \tau_2$  and the definition of  $\rightarrow$  allow us to conclude that  $e[v''/x] :_{i, \Psi'', M''} \tau_2$ .  $\square$

Next, we prove that  $\text{ref } \tau$  and  $\forall F$  are types, given appropriate  $\tau$  and  $F$ , respectively. The proofs for the remaining type constructors are given in Appendix A.

### Lemma 11 (Type ref)

If  $\tau$  is a type then  $\text{ref } \tau$  is a type.

**Proof:** We have to show that if  $\langle k, \Psi, M, v \rangle \in \text{ref } \tau$  then  $M :_k \Psi$ . But this is immediate from the definition of  $\text{ref}$ .

To show that  $\text{ref } \tau$  is closed under memory extension, suppose that  $\langle k, \Psi, M, v \rangle \in \text{ref } \tau$  and  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$ . We must prove that  $\langle j, \Psi', M', v \rangle \in \text{ref } \tau$ . There are three parts to the proof.

First, we must show that  $M' :_j \Psi'$ , but this follows directly from  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$ .

Second, by the definition of  $\text{ref}$ , since  $\langle k, \Psi, M, v \rangle \in \text{ref } \tau$ , it follows that  $v$  is some location  $\ell$  and  $\lfloor \Psi \rfloor_k(\ell) = \lfloor \tau \rfloor_k$ . The latter implies that  $\ell \in \text{dom}(\Psi)$ . Then, from  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$  it follows that  $\lfloor \Psi' \rfloor_j(\ell) = \lfloor \Psi \rfloor_k(\ell)$ , and hence,  $\ell \in \text{dom}(\Psi')$ . In addition, from  $\lfloor \Psi \rfloor_k(\ell) = \lfloor \tau \rfloor_k$  and  $j \leq k$ , it follows by definition 1 (Approx) that  $\lfloor \Psi \rfloor_j(\ell) = \lfloor \tau \rfloor_j$ , and so we may conclude that  $\lfloor \Psi' \rfloor_j(\ell) = \lfloor \tau \rfloor_j$  by transitivity of set equality.

For the third part of the proof, let  $i < j$ ; we must show that  $\langle i, \lfloor \Psi' \rfloor_i, M', M'(\ell) \rangle \in \tau$ . We concluded above that  $M' :_j \Psi'$  and that  $\ell \in \text{dom}(\Psi')$ . From  $M' :_j \Psi'$ , using the fact that  $\ell \in \text{dom}(\Psi')$  and that  $i < j$ , we may conclude that  $\langle i, \lfloor \Psi' \rfloor_i, M', M'(\ell) \rangle \in \lfloor \Psi' \rfloor_j(\ell)$ . Finally, from  $\langle i, \lfloor \Psi' \rfloor_i, M', M'(\ell) \rangle \in \lfloor \Psi' \rfloor_j(\ell)$  and  $\lfloor \Psi' \rfloor_j(\ell) = \lfloor \tau \rfloor_j$ , since  $i < j$ , it follows that  $\langle i, \lfloor \Psi' \rfloor_i, M', M'(\ell) \rangle \in \tau$ .  $\square$

### Lemma 12 (Type $\forall F$ )

If  $F$  is a function from types to types then  $\forall F$  is a type.

**Proof:** First, if  $\langle k, \Psi, M, v \rangle \in \forall F$  then  $M :_k \Psi$ . This is immediate from the definition of  $\forall F$ .

Next, to prove that  $\forall F$  is closed under memory extension, suppose that  $\langle k, \Psi, M, v \rangle \in \forall F$  and  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$ . Note that  $\langle k, \Psi, M, v \rangle \in \forall F$  implies that  $v$  is of the form  $\Lambda.e$ . We must show that  $\langle j, \Psi', M', \Lambda.e \rangle \in \forall F$ . The proof has two parts. First, we need to prove that  $M' :_j \Psi'$  but this is immediate from  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$ . For the second part of the proof, let  $(j, \Psi', M') \sqsubseteq (i, \Psi'', M'')$  and  $\text{type}(\lfloor \tau \rfloor_i)$  for some set  $\tau$  — we must show that  $e :_{i', \lfloor \Psi'' \rfloor_{i'}, M''} F(\tau)$  for all  $i' < i$ . Since memory extension is transitive (Lemma 9), we have that  $(k, \Psi, M) \sqsubseteq (i, \Psi'', M'')$ . Now, from  $\langle k, \Psi, M, \Lambda.e \rangle \in \forall F$  we may conclude that for any  $i' < i$ ,  $e :_{i', \lfloor \Psi'' \rfloor_{i'}, M''} F(\tau)$ .  $\square$

## 4 Proofs of the Typing Lemmas

We now prove each of the type inference lemmas in Figure 2. There is a type inference lemma for each case in the grammar of lambda terms (except for locations  $\ell$ ) plus two rules for the type constructor  $\mu$ . The lemma for variables, stating that  $\Gamma \models x : \Gamma(x)$ , follows immediately from the definition of  $\models$ . The type inference lemma for  $\mathbf{0}$  stating  $\Gamma \models \mathbf{0} : \mathbf{unit}$  follows directly from the definition of  $\mathbf{unit}$ .

In this section we prove the type theorems for type abstraction and type application. Proofs for the remaining type-checking rules in Figure 2 are given in Appendix B (Theorems 32–42).

### Lemma 13 (Closed Type Application)

If  $e$  is a closed term,  $\tau$  is a type, and  $F$  is a function from types to types such that  $e :_{k, \Psi, M} \forall F$ , then  $e[] :_{k, \Psi, M} F(\tau)$ .

**Proof:** We must prove  $e[] :_{k, \Psi, M} F(\tau)$  under the premises of the lemma. Since  $F$  is a function from types to types, by Lemma 12  $\forall F$  is a type. Since  $e :_{k, \Psi, M} \forall F$  we have that  $(M, e)$  is safe for  $k$  steps and if  $(M, e)$  reduces to  $(M', v)$  (where  $v$  is a value) in fewer than  $k$  steps, then  $v$  must be of the form  $\Lambda.e'$ . Hence, the state  $(M, e[])$  either reduces for  $k$  steps without reaching a state of the form  $(M', v[])$  or there exists  $e'$  and  $M'$  such that  $(M, e[]) \mapsto^j (M', (\Lambda.e')[])$  with  $j < k$ . In the first case we have that  $(M, e[])$  is safe for  $k$  steps and  $(M, e)$  does not reduce to a value in fewer than  $k$  steps and hence  $e[] :_{k, \Psi, M} F(\tau)$ . In the second case, it follows from the operational semantics in Figure 1 that  $(M, e) \mapsto^j (M', \Lambda.e')$ . Since  $e :_{k, \Psi, M} \forall F$ , definition 5 (Expr : Type) implies that there exists a memory typing  $\Psi'$  such that  $(k, \Psi, M) \sqsubseteq (k - j, \Psi', M')$  and  $\langle k - j, \Psi', M', \Lambda.e' \rangle \in \forall F$ .

Next, pick the memory typing  $\lfloor \Psi' \rfloor_{k-j-1}$ . Then, the following information-forgetting memory extension holds:  $(k - j, \Psi', M') \sqsubseteq (k - j - 1, \lfloor \Psi' \rfloor_{k-j-1}, M')$ . From  $\text{type}(\tau)$  it follows by definitions 4 and 1 (Type and Approx) that  $\text{type}(\lfloor \tau \rfloor_{k-j-1})$ . Then from  $\Lambda.e' :_{k-j, \Psi', M'} \forall F$  and the definition of  $\forall F$  we may conclude that for any  $i < (k - j - 1)$ ,  $e' :_{i, \lfloor \Psi' \rfloor_{k-j-1}, M'} F(\tau)$  — that is,  $e' :_{i, \lfloor \Psi' \rfloor_i, M'} F(\tau)$  (from  $i < (k - j - 1)$  and definition 1 (Approx)).

Since  $\tau$  is a type,  $F(\tau)$  is a type. Also, the following information-forgetting memory extension holds:  $(k - j - 1, \lfloor \Psi' \rfloor_{k-j-1}, M') \sqsubseteq (i, \lfloor \Psi' \rfloor_i, M')$ . Then, first, by Lemma 9 we have  $(k, \Psi, M) \sqsubseteq (i, \lfloor \Psi' \rfloor_i, M')$ . Second, since  $(M'.(\Lambda.e')[]) \mapsto (M', e')$ , we have  $(M, e[]) \mapsto^{j+1} (M', e')$ . Third, we have  $e' :_{i, \lfloor \Psi' \rfloor_i, M'} F(\tau)$  where  $\text{type}(F(\tau))$ . These three statements imply (since they hold for any  $i < (k - (j + 1))$ ) that  $e[] :_{k, \Psi, M} F(\tau)$ .  $\square$

### Theorem 14 (Type Application)

Let  $\Gamma$  be a context, let  $F$  be a function from types to types, and let  $\tau$  be a type. If  $\Gamma \models e : \forall F$  then  $\Gamma \models e[] : F(\tau)$ .

**Proof:** We must prove that for any  $k \geq 0$  we have  $\Gamma \models_k e[] : F(\tau)$ . More specifically, for any  $M, \Psi$ , and  $\sigma$  such that  $M :_k \Psi$  and  $\sigma :_{k, \Psi, M} \Gamma$ , we must show  $\sigma(e[]) :_{k, \Psi, M} F(\tau)$ . Suppose  $M :_k \Psi$  and  $\sigma :_{k, \Psi, M} \Gamma$ . By the premise of the theorem we have  $\sigma(e) :_{k, \Psi, M} \forall F$  and  $\text{type}(\tau)$ . The result now follows from Lemma 38.  $\square$

Before we can prove the typing rules for type abstraction, we must define the notion of a nonexpansive functional.

### Definition 15

A nonexpansive functional is a function  $F$  from types to types such that for any type  $\tau$  and  $k \geq 0$  we have

$$[F(\tau)]_k = [F([\tau]_k)]_k$$

The term “nonexpansive” is explained in more detail by Appel and McAllester, who show that all the functionals that can be built by compositions of our type constructors are nonexpansive.

### Theorem 16 (Type Abstraction)

Let  $\Gamma$  be a context and let  $F$  be a nonexpansive type functional. If  $\Gamma \models e : F(\tau)$  for any type set  $\tau$ , then  $\Gamma \models \Lambda.e : \forall F$ .

**Proof:** We must show that for any  $k \geq 0$  and  $M, \Psi$  and  $\sigma$  such that  $M :_k \Psi$  and  $\sigma :_{k, \Psi, M} \Gamma$  we have  $\sigma(\Lambda.e) :_{k, \Psi, M} \forall F$ . Suppose  $M :_k \Psi$  and  $\sigma :_{k, \Psi, M} \Gamma$ . Let  $j, v, \Psi', M'$ , and  $\tau$  be such that  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$  and  $\text{type}([\tau]_j)$ . By the definition of  $\forall$  it suffices to show that for any  $i < j$  we have  $\sigma(e) :_{i, [\Psi']_i, M'} F(\tau)$ . Since  $[\tau]_j$  is a type, given the premise of theorem it follows that  $\Gamma \models e : F([\tau]_j)$ . Hence  $\sigma(e) :_{k, \Psi, M} F([\tau]_j)$ . Also, since  $F$  is a function from types to types,  $F([\tau]_j)$  is a type.

The following information-forgetting memory extension holds:  $(j, \Psi', M') \sqsubseteq (i, [\Psi']_i, M')$ . Then by Lemma 9 it follows that  $(k, \Psi, M) \sqsubseteq (i, [\Psi']_i, M')$ . Hence, from  $\sigma(e) :_{k, \Psi, M} F([\tau]_j)$  and  $\text{type}(F([\tau]_j))$  we can conclude that  $\sigma(e) :_{i, [\Psi']_i, M'} F([\tau]_j)$ . Now since  $i < j$ , definitions 1, 4, and 5 (Approx, Type, and Expr : Type) allow us to conclude that  $\sigma(e) :_{i, [\Psi']_i, M'} [F([\tau]_j)]_j$ . Using the premise that  $F$  is nonexpansive we have that  $\sigma(e) :_{i, [\Psi']_i, M'} [F(\tau)]_j$ . But since  $i < j$ , definition 1 (Approx) implies that  $\sigma(e) :_{i, [\Psi']_i, M'} F(\tau)$ .  $\square$

## 5 Representation in CiC

At the beginning of Section 2, we informally defined types as collections of tuples  $\langle k, \Psi, M, v \rangle$ , where in particular  $\Psi$  is a mapping from location to types. This informal

definition characterizes a proper class rather than a set, since for example it allows pathological cases like:

$$\langle k, \Psi, M, v \rangle \in \tau \text{ and } \Psi(\ell) = \tau$$

Any attempt toward a set-theoretic formalization starting from this definition is therefore doomed to failure.

What makes such a formalization possible is that all definitions given in Section 2 obey the following invariant: when considering a tuple  $\langle k, \Psi, M, v \rangle$  we do not require  $\Psi$  to be defined beyond  $[\Psi]_k$ . Hence our types do indeed form a set, which can be constructed using recursively defined sets  $\text{Types}_k$  and  $\text{MemTypes}_k$  as follows:

$$\begin{aligned} \tau \in \text{Types}_0 &\text{ iff } \tau = \{\} \\ \tau \in \text{Types}_{k+1} &\text{ iff } \forall \langle j, \Psi, M, v \rangle \in \tau. j \leq k \wedge \Psi \in \text{MemTypes}_j \\ \Psi \in \text{MemTypes}_k &\text{ iff } \forall \ell \in \text{dom}(\Psi). \Psi(\ell) \in \text{Types}_k \\ \tau \in \text{Types} &\text{ iff } \forall k. [\tau]_k \in \text{Types}_k. \end{aligned}$$

The Calculus of Inductive Constructions, upon which the Coq system is based, can represent the above definitions quite directly:

```
Fixpoint itype [k : nat] : Type :=
  Cases k
  of 0 => UnitT
  | (S k') =>
    (prodT (itype k')
      ((location -> (itype k'))
        -> memory -> exp -> Prop))
end.
```

```
Definition imemtype [k : nat] : Type :=
  location -> (itype k).
```

```
Definition type: Type := (k: nat) (itype k).
```

Each set  $\text{Types}_k$  is modeled using a product type  $(\text{itype } k)$  in CiC. More specifically,  $\text{Types}_0$  is modeled by the unit type  $\text{UnitT}$ , while  $\text{Types}_{k+1}$  is given by the product of the representation of  $\text{Types}_k$  and the set of membership functions for triples  $\langle \Psi, M, v \rangle$ , where  $\Psi$  has type  $(\text{imemtype } k)$ .

Given an object  $\tau$  of type  $\text{type}$ , its  $k$ -th approximation will correspond to the application  $(\tau \text{ } k)$ . To check that a 4-tuple  $\langle k, \Psi, M, v \rangle$  is in  $\tau$ , we will need to apply  $\tau$  to  $k + 1$ , and take the second component:

$$(\text{sndT } (\tau \text{ } (S \text{ } k)) \text{ } \Psi \text{ } M \text{ } v) : \text{Prop}$$

The definition of the types and type constructors presented in Figure 3 can be given by recursion on the natural numbers. At each step, we have to construct an object of type  $(\text{itype } k)$ . The case  $k = 0$  is trivial, since  $\text{UnitT}$  is the only object belonging to  $(\text{itype } 0)$ . For the case  $k + 1$ , we only have to provide a formula which decides which tuples  $\langle k, \Psi, M, v \rangle$  will belong to the type.

Figure 4 illustrates the representation for the type constructor for mutable references. The predicate

```

Definition refTy [tau : type] : type :=
  (nat_rect itype IT
    ([k : nat][tauk : (itype k)]
      (pairT tauk
        ([psi: (imemtype k)][m: memory][v: exp]
          (Ex [l : location]
            (Ex [v' : exp]
              (v = (loc l))
              /\ ((m l) = (Some exp v'))
              /\ (imemtype_sat k psi m)
              /\ (eqT (itype k) (psi l) (tau k))
              /\ (All [j : nat]
                (h : (lt j k))
                ((sndT (tau (S j)))
                  (imemtype_approx k j h psi)
                  m v')))))))).

```

**Figure 4. Definition of ref in CiC**

```

imemtype_sat : (k:nat)
              (imemtype k) -> memory
              -> Prop

```

models the relation  $M :_k \Psi$ , while we use the function

```

imemtype_approx : (k, j:nat)
                  (lt j k) -> (imemtype k)
                  -> (imemtype j)

```

to “lower” an approximation to the correct index. Both of these are straightforwardly defined in Coq.

## 6 Representation in H.O.L.

Higher-order logic does not provide as convenient a mechanism for making stratified metalogical types; we must construct the stratification ourselves. We do so by constructing a Gödelization of type expressions.

A naive Gödelization would proceed as follows. We want a relation  $\rho$  between the natural numbers and all the type expressions that can be constructed from the operators in Figure 3. Unfortunately, the definitions of some of those operators would refer to  $\rho$ , leading to a circularity. We resolve this circularity by constructing a hierarchy of relations; the semantics of the types in one level of the hierarchy can make use of lower levels. We now present the metalogical types, in higher-order logic, of what we will construct:

```

Exp      = the type of lambda-expressions
Loc      = Nat
Mem      = Loc  $\overset{\text{fin}}{\rightarrow}$  Exp
Term     = Nat
MemType  = Loc  $\overset{\text{fin}}{\rightarrow}$  Term
Type     = Nat  $\times$  MemType  $\times$  Mem  $\times$  Exp  $\rightarrow$  o
Rep      = Term  $\rightarrow$  Type

```

*Exp*  $e$  is a lambda-expression; *Loc*  $\ell$  is an addressable memory location; *Mem*  $M$  is a memory; *Term*  $t$  is a Gödel number; *MemType*  $\Psi$  is a memory typing, but in this model

it maps locations to terms instead of types; *Type*  $\tau$  is a set of 4-tuples, as before; *Rep*  $\rho$  is a representation function (Gödel numbering).

### Definition 17 (Approx)

We define  $[\tau]_k$  as before. However, since now memory typing maps locations into terms, we need to parametrize the approximation of a memory typing with respect to a representation function:

$$[\Psi]_{\rho,k} \equiv \{(\ell \mapsto [\rho(t)]_k) \mid \Psi(\ell) = t\}$$

Note that the metalogical type of  $[\Psi]_{\rho,k}$  is  $Loc \rightarrow Type$ , which is different than the metalogical type of  $\Psi$ .

All the definitions given in Section 2 follow through, but need to be parametrized by  $\rho$  as well.

### Definition 18 (Well-typed Memory)

$$\begin{aligned}
M :_{\rho,k} \Psi &\equiv \\
\text{dom}(\Psi) &\subseteq \text{dom}(M) \wedge \text{finite}(\text{dom}(M)) \wedge \\
\forall j < k. \forall \ell \in \text{dom}(\Psi). & \\
\exists \Psi'. [\Psi']_{\rho,j} = [\Psi]_{\rho,j} &\wedge \langle j, \Psi', M, M(\ell) \rangle \in [\Psi]_{\rho,j}(\ell)
\end{aligned}$$

### Definition 19 (Memory Extension)

$$\begin{aligned}
(k, \Psi, M) \sqsubseteq_{\rho} (j, \Psi', M') &\equiv \\
j \leq k \wedge (\forall \ell \in \text{dom}(\Psi). [\Psi']_{\rho,j}(\ell) &= [\Psi]_{\rho,j}(\ell)) \wedge M' :_{\rho,j} \Psi'
\end{aligned}$$

### Definition 20 (Type)

We say  $\text{type}_{\rho}(\tau)$  if whenever  $\langle k, \Psi, M, v \rangle \in \tau$  we have  $M :_{\rho,k} \Psi$  and if  $(k, \Psi, M) \sqsubseteq_{\rho} (j, \Psi', M')$  then  $\langle j, \Psi', M', v \rangle \in \tau$ .

### Definition 21 (Expr : Type)

$$\begin{aligned}
e :_{\rho,k,\Psi,M} \tau &\equiv \forall j, M', e'. (0 \leq j < k \wedge (M, e) \mapsto^j (M', e') \\
&\quad \wedge \text{irred}(M', e')) \\
&\Rightarrow \exists \Psi'. (k, \Psi, M) \sqsubseteq_{\rho} (k - j, \Psi', M') \\
&\quad \wedge \langle k - j, \Psi', M', e' \rangle \in \tau
\end{aligned}$$

We define *pretype* constructors, from which we will later define *type* constructors. Each *pretype* constructor (written with an *overbar*) needs a  $\rho$  parameter.

$$\begin{aligned}
\overline{\perp}_{\rho} &\equiv \{\} \\
\overline{\text{unit}}_{\rho} &\equiv \{\langle k, \Psi, M, \mathbf{0} \rangle \mid M :_{\rho,k} \Psi\} \\
\overline{\tau_1} \overline{\tau_2}_{\rho} &\equiv \{\langle k, \Psi, M, \lambda x. e \rangle \mid M :_{\rho,k} \Psi \wedge \\
&\quad \forall j < k. \forall v, \Psi', M'. \\
&\quad ((k, \Psi, M) \sqsubseteq_{\rho} (j, \Psi', M') \wedge \langle j, \Psi', M', v \rangle \in \tau_1) \\
&\quad \Rightarrow e[v/x] :_{\rho,j,\Psi',M'} \tau_2\} \\
\overline{\mu}_{\rho} F &\equiv \{\langle k, \Psi, M, v \rangle \mid \langle k, \Psi, M, v \rangle \in F^{k+1}(\perp_{\rho})\} \\
\overline{\text{ref}}_{\rho} \tau &\equiv \{\langle k, \Psi, M, \ell \rangle \mid M :_{\rho,k} \Psi \wedge [\Psi]_{\rho,k}(\ell) = [\tau]_k \wedge \\
&\quad \forall j < k. \langle j, \Psi, M, M(\ell) \rangle \in \tau\} \\
\overline{\forall}_{\rho} F &\equiv \{\langle k, \Psi, M, \Lambda. e \rangle \mid M :_{\rho,k} \Psi \wedge \\
&\quad \forall j, \Psi', M', \tau. ((k, \Psi, M) \sqsubseteq_{\rho} (j, \Psi', M') \wedge \text{type}_{\rho}([\tau]_j)) \\
&\quad \Rightarrow \forall i < j. e :_{\rho,i,\Psi',M'} F(\tau)\} \\
\overline{\exists}_{\rho} F &\equiv \{\langle k, \Psi, M, \text{pack } v \rangle \mid M :_{\rho,k} \Psi \wedge \\
&\quad \exists \tau. \text{type}_{\rho}([\tau]_k) \wedge \forall j < k. \langle j, \Psi, M, v \rangle \in F(\tau)\}
\end{aligned}$$



Now, having defined the pretype constructors, we are free to Gödelize them. Level 0 of the hierarchy is a relation that maps every Gödel number to the bottom type, that is,  $\text{rep}_0(t) = \{\}$ . Level  $i + 1$  of the hierarchy is defined as follows:

$$\begin{aligned} \text{rep}_{i+1}(\langle 0, 0 \rangle) &= \overline{\perp}_{\text{rep}_i} \\ \text{rep}_{i+1}(\langle 2, 0 \rangle) &= \overline{\text{unit}}_{\text{rep}_i} \\ \text{rep}_{i+1}(\langle 3, \langle t_1, t_2 \rangle \rangle) &= \text{rep}_{i+1}(t_1) \overline{\multimap}_{\text{rep}_i} \text{rep}_{i+1}(t_2) \\ \dots & \\ \text{rep}_{i+1}(\langle 5, t \rangle) &= \overline{\text{ref}}_{\text{rep}_i}(\text{rep}_{i+1}(t)) \\ \dots & \end{aligned}$$

We use the notation  $\langle i, j \rangle$  for the injective mapping from pairs of natural numbers to the natural numbers. We don't show here the representation of  $\overline{\mu}F$ ,  $\overline{\forall}F$  and  $\overline{\exists}F$  because this would require a Gödelization of type-functions as well as types. The way we handle this in the proof of a full-scale type system is to Gödelize type expressions (with free deBruijn variables) instead of types.

**Definition 22 (Type constructors)**

$$\begin{aligned} \perp &= \bigcup_k [\overline{\perp}_{\text{rep}_k}]_k & \text{unit} &= \bigcup_k [\overline{\text{unit}}_{\text{rep}_k}]_k \\ \tau_1 \rightarrow \tau_2 &= \bigcup_k [\tau_1 \overline{\multimap}_{\text{rep}_k} \tau_2]_k \\ \mu F &= \bigcup_k [\overline{\mu}_{\text{rep}_k} F]_k & \text{ref } \tau &= \bigcup_k [\overline{\text{ref}}_{\text{rep}_k} \tau]_k \\ \forall F &= \bigcup_k [\overline{\forall}_{\text{rep}_k} F]_k & \exists F &= \bigcup_k [\overline{\exists}_{\text{rep}_k} F]_k \end{aligned}$$

**Definition 23 (Semantics of Judgment)**

For any value context  $\Gamma$  and value substitution  $\sigma$  we write  $\sigma :_{\rho, k, \Psi, M} \Gamma$  (“ $\sigma$  approximately obeys  $\Gamma$ ”) if for all variables  $x \in \text{dom}(\Gamma)$  we have  $\sigma(x) :_{\rho, k, \Psi, M} \Gamma(x)$ .

We write  $\Gamma \models_k e : \tau$  to mean that every free variable of  $e$  is mapped by  $\Gamma$  and

$$\begin{aligned} \forall \sigma, \Psi, M. (M :_{\text{rep}_k, k} \Psi \wedge \sigma :_{\text{rep}_k, k, \Psi, M} \Gamma) \\ \Rightarrow \sigma(e) :_{\text{rep}_k, k, \Psi, M} \tau \end{aligned}$$

**Theorem 24**

Using these definitions as the interpretation of the typing operators, all the rules of Figure 2 hold, as well as the statement of Theorem 8 (typability implies safety).

**Proof:** The proof corresponds closely the the proof shown in Section 4. We are implementing a machine-checked version of this higher-order-logic proof in the Twelf system. That proof is for von Neumann machines instead of for lambda-calculus, since our application is in proof-carrying code for a real machine.  $\square$

To better explain the correspondence between our higher-order logic proof and the proof shown in Section 4, we make a few remarks.

All (positive) representation levels are defined on the same set of terms, which constitutes the set of valid terms. Intuitively, each valid term  $t$  corresponds bijectively with a

type  $\tau$  freely built using the constructors of Figure 3, and increasing representation levels offer us increasingly better approximations of that type  $\tau$ . This idea is formalized by the following key result:

**Lemma 25**

Let  $t$  be a term, and  $\tau$  its corresponding type. For each  $i$  we have  $[\tau]_i = [\text{rep}_i(t)]_i$ .

**Proof:** By a nested induction argument. The primary induction is on  $i$ , the secondary one on the structure of  $t$ . The case  $i = 0$  is trivial, since  $[\tau]_0 = \{\} = [\text{rep}_0(t)]_0$ . The case for  $(i + 1)$  is done by case analysis on the structure of  $t$ .  $\square$

## 7 Eliminating Noncomputational Steps

Our application for this semantics is in a proof-carrying code system that can provide safety proofs (derived from type-checking) for an ordinary machine-language program, where the machine itself has no notion of types. In  $\lambda$ -calculus terms, we would like a semantics of types that can yield safety proofs for an entirely untyped operational lambda-calculus. For example, we wish to avoid explicit fold and unfold steps in calculating with recursive types, and indeed the type system we have demonstrated is equirecursive ( $\mu F$  is equal to  $F(\mu F)$ ) instead of isorecursive ( $\mu F$  isomorphic, via fold/unfold, to  $F(\mu F)$ ).

However, our operational semantics in Figure 1 has explicit “run-time” steps for opening an existential and applying a universal ( $\text{open}(\text{pack } v)$  and  $(\Lambda.e)[\ ]$ ). A real machine has no such instructions.

We would like to eliminate  $e[\ ]$  and  $\text{open}$  from our operational calculus and use typing rules like this:

$$\frac{\forall \tau. \text{type}(\tau) \Rightarrow \Gamma \models e : F(\tau)}{\Gamma \models e : \forall F} \text{ (tabs')}$$

$$\frac{\Gamma \models e : \forall F \quad \text{type}(\tau)}{\Gamma \models e : F(\tau)} \text{ (tapp')}$$

But there's a minor technical problem. Consider the statement of Lemma 13: If  $e$  is a closed term and  $F$  is a type functional such that  $e :_{k, \Psi, M} \forall F$  then, for any type set  $\tau$ ,  $e[\ ] :_{k, \Psi, M} F(\tau)$ . If we restate this to conclude that  $e :_{k, \Psi, M} F(\tau)$ , the lemma will not hold; in fact, all we can prove is  $e :_{k-1, \Psi, M} F(\tau)$ . If we view the index  $k$  as counting the number of computation steps that it's safe to execute, then applying a universal uses up one computation step. The reason can be seen in the definition of the  $\forall F$  operator (Figure 3); in judging  $v :_{k, \Psi, M} \forall F$ , we judge whether  $v :_{i, \Psi, M} F(\tau)$  where  $i$  is strictly less than  $k$ . We have no information about whether  $v :_{k, \Psi, M} F(\tau)$ .

It is for this reason that we put explicit computation steps for applying a universal and for unpacking an existential into our operational calculus: it makes the proof simpler.

But in our prototype proof-carrying code system, these extra computational steps are ugly: they require the compiler to generate a no-op instruction each time it unpacks an existential. The purpose of this no-op is to use up one computational step to satisfy the proof.

To eliminate this noncomputational step from our lambda-calculus model, we can simply change the statement of our lemma to read: If  $\forall k.(e :_{k,\Psi,M} \forall F)$  then  $\forall k.(e :_{k,\Psi,M} F(\tau))$ . This weaker lemma will be enough to prove the typing judgment (tapp') shown above.

However, the way our model of types for von Neumann machines is structured, this weaker lemma is not as useful. When we reason about recursive functions, we want to prove that “if this computation is safe for  $k$  steps, then it’s safe for  $k + 1$  steps;” in such a proof, we can’t easily assume that  $e$  is safe for arbitrary  $k$ .

We will sketch a solution. We will define a step relation that allows real steps (from the specification of the real machine) and artificial steps (type application and open); we ensure that at most a bounded number of artificial steps can be taken between real steps (by decrementing an artificial counter in the state). For any program  $e$ , there is a number  $N$  such that  $e$  never executes more than  $N$  consecutive artificial steps. In our model, we will define a typing judgement  $e :_{N,k,\Psi,M} \tau$  that means,  $e$  executes no more than  $N$  consecutive artificial steps before the first real step, and then executes no more than  $N$  artificial steps between real steps; and is safe for  $k$  real steps.

## 8 Conclusion

The indexed model of types was derived by considering the notion of approximations inherent in domain theory. But the particular advantage of the indexed model is that it permits simple and direct proofs, without the need to “import” large mathematical theories, such as domain theory or category theory.

We have successfully adapted the indexed model to the difficult task of modeling impredicative-polymorphic mutable references. We continue to be guided by the domain-theoretic idea of approximating everything in sight. The resulting proofs, though more complicated than those for a model without references, are still short enough to permit implementation as machine-checked proofs in a simple higher-order logic, or in the calculus of inductive constructions.

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## A Proofs of Types

### Lemma 9 (Memory Extension Transitive)

If  $(k_1, \Psi_1, M_1) \sqsubseteq (k_2, \Psi_2, M_2)$  and  $(k_2, \Psi_2, M_2) \sqsubseteq (k_3, \Psi_3, M_3)$  then  $(k_1, \Psi_1, M_1) \sqsubseteq (k_3, \Psi_3, M_3)$ .

**Proof:** The proof is in three parts. First, from the premises of the lemma and the definition of  $\sqsubseteq$  we have  $k_2 \leq k_1$  and  $k_3 \leq k_2$ . It follows that  $k_3 \leq k_1$ . Second, suppose  $\ell \in \text{dom}(\Psi_1)$ . Then the first premise of the lemma allows us to conclude that  $\lfloor \Psi_2 \rfloor_{k_2}(\ell) = \lfloor \Psi_1 \rfloor_{k_2}(\ell)$ . It follows that  $\ell \in \text{dom}(\Psi_2)$ . Then, from the second premise of the lemma we have  $\lfloor \Psi_3 \rfloor_{k_3}(\ell) = \lfloor \Psi_2 \rfloor_{k_3}(\ell)$ . From  $\lfloor \Psi_2 \rfloor_{k_2}(\ell) = \lfloor \Psi_1 \rfloor_{k_2}(\ell)$  and  $k_3 \leq k_2$  it follows that  $\lfloor \Psi_2 \rfloor_{k_3}(\ell) = \lfloor \Psi_1 \rfloor_{k_3}(\ell)$  (by definition 1 (Approx)). Hence we may conclude that  $\lfloor \Psi_3 \rfloor_{k_3}(\ell) = \lfloor \Psi_1 \rfloor_{k_3}(\ell)$  by transitivity of set equality. For the third part of our proof we must show that  $M_3 \text{ :}_{k_3} \Psi_3$  but this is immediate from the second premise of the lemma.  $\square$

### Lemma 10 (Type $\tau_1 \rightarrow \tau_2$ )

If  $\tau_1$  and  $\tau_2$  are types then  $\tau_1 \rightarrow \tau_2$  is also a type.

**Proof:** Given in Section 3.  $\square$

Before we can prove that  $\mu F$  is a type, we must define the notion of a well-founded functional.

### Definition 26 (Well Founded)

A well founded functional is a function  $F$  from types to types such that for any type  $\tau$  and  $k \geq 0$  we have

$$\lfloor F(\tau) \rfloor_{k+1} = \lfloor F(\lfloor \tau \rfloor_k) \rfloor_{k+1}$$

Appel and McAllester [5] show that type functions  $\text{ref}$  and  $\rightarrow$  are well founded, and that the composition of well founded and nonexpansive functionals (in any order) is well founded. Note that if  $F$  is a function from types to types and  $\tau$  is a type then  $F^k(\tau)$  is a type for any  $k \geq 0$ .

### Lemma 27

For  $F$  well founded and  $j \leq k$ , for any  $\tau, \tau_1, \tau_2$ ,

$$(1) \lfloor F^j(\tau_1) \rfloor_j = \lfloor F^j(\tau_2) \rfloor_j$$

$$(2) \lfloor F^j(\tau) \rfloor_j = \lfloor F^k(\tau) \rfloor_j$$

**Proof:** (1) By induction.

$$\begin{aligned} \lfloor F^j(\tau_1) \rfloor_0 &= \perp = \lfloor F^j(\tau_2) \rfloor_0. \\ \lfloor F^{j+1}(\tau_1) \rfloor_{j+1} &= \\ \lfloor F(F^j(\tau_1)) \rfloor_{j+1} &= \\ \lfloor F(\lfloor F^j(\tau_1) \rfloor_j) \rfloor_{j+1} &= \\ \lfloor F(\lfloor F^j(\tau_2) \rfloor_j) \rfloor_{j+1} &= \\ \lfloor F(F^j(\tau_2)) \rfloor_{j+1} &= \\ \lfloor F^{j+1}(\tau_2) \rfloor_{j+1}. & \end{aligned}$$

(2) Using (1), taking  $\tau_2 = F^{k-j}(\tau_1)$ .  $\square$

This says that  $j$  applications of a well-founded functional to any type yields the same thing, to approximation  $j$ .

### Lemma 28 (Type $\mu F$ )

If  $F$  is well founded, then  $\mu F$  is a type.

**Proof:** We have to prove that if  $\langle k, \Psi, M, v \rangle \in \mu F$  then  $M \text{ :}_k \Psi$ . By the definition of  $\mu F$  we have  $\langle k, \Psi, M, v \rangle \in F^{k+1}(\perp)$ . Since  $\perp$  is a type and  $F$  is a function from types to types, we have that  $F^{k+1}(\perp)$  is a type. Then  $\langle k, \Psi, M, v \rangle \in F^{k+1}(\perp)$  allows us to conclude that  $M \text{ :}_k \Psi$ .

Next, we must show that  $\mu F$  is closed under memory extension. Suppose that  $\langle k, \Psi, M, v \rangle \in \mu F$  and that  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$ .

$$\begin{array}{ll} \langle k, \Psi, M, v \rangle \in \mu F & \\ \langle k, \Psi, M, v \rangle \in F^{k+1}(\perp) & \text{by def'n of } \mu F \\ \langle j, \Psi', M', v \rangle \in F^{k+1}(\perp) & \text{by def'n 4 (Type)} \\ \langle j, \Psi', M', v \rangle \in \lfloor F^{k+1}(\perp) \rfloor_{j+1} & \text{by def'n 1 (Approx)} \\ \langle j, \Psi', M', v \rangle \in \lfloor F^{j+1}(\perp) \rfloor_{j+1} & \text{by Lemma 27} \\ \langle j, \Psi', M', v \rangle \in F^{j+1}(\perp) & \text{by def'n 1 (Approx)} \\ \langle j, \Psi', M', v \rangle \in \mu F & \text{by def'n of } \mu F \end{array}$$

$\square$

### Lemma 11 (Type ref)

If  $\tau$  is a type then  $\text{ref } \tau$  is a type.

**Proof:** Given in Section 3.  $\square$

### Lemma 12 (Type $\forall F$ )

If  $F$  is a function from types to types then  $\forall F$  is a type.

**Proof:** Given in Section 3.  $\square$

### Lemma 29

If  $\text{type}(\lfloor \tau \rfloor_k)$  and  $j \leq k$  then  $\text{type}(\lfloor \tau \rfloor_j)$ .

**Proof:** Immediate from definitions 4 (Type) and 1 (Approx).  $\square$

### Lemma 30

If  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$ ,  $i < k$ , and  $i < j$ , then  $(i, \lfloor \Psi \rfloor_i, M) \sqsubseteq (i, \lfloor \Psi' \rfloor_i, M')$ .

**Proof:** Immediate from definitions 3 (Memory Extension) and 1 (Approx).  $\square$

**Lemma 31 (Type  $\exists F$ )**

If  $F$  is a nonexpansive functional then  $\exists F$  is a type.

**Proof:** We must show that if  $\langle k, \Psi, M, v \rangle \in \exists F$  then  $M :_k \Psi$ , but this follows directly from the definition of  $\exists F$ .

To prove that  $\exists F$  is closed under memory extension, suppose that  $\langle k, \Psi, M, v \rangle \in \exists F$  and  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$ . Note that  $\langle k, \Psi, M, v \rangle \in \exists F$  implies that  $v$  is of the form  $\text{pack } v'$  where  $v'$  is a value. We must show that  $\langle j, \Psi', M', \text{pack } v' \rangle \in \exists F$ . First, we have to show  $M' :_j \Psi'$ . But this is immediate from  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$ . Second, we must prove the existence of some set  $\tau$  such that  $\text{type}(\lfloor \tau \rfloor_j)$ . From  $\langle k, \Psi, M, \text{pack } v' \rangle \in \exists F$  it follows by the definition of  $\exists F$  that there exists some  $\tau$  such that  $\text{type}(\lfloor \tau \rfloor_k)$ . Since  $j \leq k$  it follows by Lemma 29 that  $\text{type}(\lfloor \tau \rfloor_j)$ .

Finally, let  $i < j$ ; we must show  $\langle i, \lfloor \Psi' \rfloor_i, M', v' \rangle \in F(\tau)$ . Since  $i < j$  and  $j \leq k$  we have  $i < k$ , and from  $\langle k, \Psi, M, \text{pack } v' \rangle \in \exists F$  we have  $\langle i, \lfloor \Psi \rfloor_i, M, v' \rangle \in F(\tau)$ . Since  $F$  is a function from types to types and  $\lfloor \tau \rfloor_j$  is a type,  $F(\lfloor \tau \rfloor_j)$  is a type. It follows that  $\lfloor F(\tau) \rfloor_j$  is a type (by definition 1 (Approx)). Since  $F$  is nonexpansive, we have that  $\lfloor F(\lfloor \tau \rfloor_j) \rfloor_j = \lfloor F(\tau) \rfloor_j$ . Hence  $\lfloor F(\tau) \rfloor_j$  is a type. Now, since  $\langle i, \lfloor \Psi \rfloor_i, M, v' \rangle \in F(\tau)$  and  $i < j$ , it follows that  $\langle i, \lfloor \Psi \rfloor_i, M, v' \rangle \in \lfloor F(\tau) \rfloor_j$ . Furthermore, since  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$ ,  $i < j$ , and  $i < k$ , by Lemma 30 we may conclude that  $(i, \lfloor \Psi \rfloor_i, M) \sqsubseteq (i, \lfloor \Psi' \rfloor_i, M')$ . Since types are closed under memory extension, from  $\langle i, \lfloor \Psi \rfloor_i, M, v' \rangle \in \lfloor F(\tau) \rfloor_j$  and  $\text{type}(\lfloor F(\tau) \rfloor_j)$  we may conclude that  $\langle i, \lfloor \Psi' \rfloor_i, M', v' \rangle \in \lfloor F(\tau) \rfloor_j$ . But since  $i < j$  it follows that  $\langle i, \lfloor \Psi' \rfloor_i, M', v' \rangle \in F(\tau)$ .  $\square$

## B Proofs of Typing Rules

We prove each of the type-checking rules given in Figure 2 as theorems.

**Lemma 32 (Closed Application)**

If  $e_1$  and  $e_2$  are closed terms and  $\tau_1$  and  $\tau_2$  are type sets such that  $e_1 :_{k, \Psi, M} \tau_1 \rightarrow \tau_2$  and  $e_2 :_{k, \Psi, M} \tau_1$  then  $(e_1 e_2) :_{k, \Psi, M} \tau_2$ .

**Proof:** Since  $e_1 :_{k, \Psi, M} \tau_1 \rightarrow \tau_2$  and  $e_2 :_{k, \Psi, M} \tau_1$  we immediately have that both  $(M, e_1)$  and  $(M, e_2)$  are safe for  $k$  steps and that if  $(M, e_1)$  reduces to some  $(M', v_1)$ , where  $v_1$  is a value, in fewer than  $k$  steps, the value  $v_1$  must be a lambda expression. Hence, the state  $(M, (e_1 e_2))$  either reduces for  $k$  steps without any top-level beta-reduction, or there exists a lambda expression  $\lambda x.e$ , a value  $v$ , and a memory  $M'$  such that  $(M, (e_1 e_2)) \mapsto^j (M', (\lambda x.e) v)$  with  $j < k$ . In the first case we have that  $(M, (e_1 e_2))$  is safe for  $k$  steps and does not generate a value in less than  $k$  steps and hence  $(e_1 e_2) :_{k, \Psi, M} \tau_2$  (for any  $\tau_2$ ).

In the second case, since  $e_1 :_{k, \Psi, M} \tau_1 \rightarrow \tau_2$  and  $e_2 :_{k, \Psi, M} \tau_1$ , definition 5 (Expr : Type) implies that there exists a memory typing  $\Psi'$  such that  $(k, \Psi, M) \sqsubseteq (k - j, \Psi', M')$ ,  $\langle k - j, \Psi', M', \lambda x.e \rangle \in \tau_1 \rightarrow \tau_2$ , and  $\langle k - j, \Psi', M', v \rangle \in \tau_1$ . Pick memory typing  $\lfloor \Psi' \rfloor_{k-j-1}$ . Then the following memory extension (where we simply forget some information) holds:  $(k - j, \Psi', M') \sqsubseteq (k - j - 1, \lfloor \Psi' \rfloor_{k-j-1}, M')$ . Since  $\langle k - j, \Psi', M', v \rangle \in \tau_1$  and  $\tau_1$  is a type — i.e., by definition 4 (Type)  $\tau_1$  is closed under memory extension — we may now conclude that  $\langle k - j - 1, \lfloor \Psi' \rfloor_{k-j-1}, M', v \rangle \in \tau_1$ . The definition of  $\rightarrow$  then implies that  $e[v/x] :_{k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M'} \tau_2$ . But we now have  $(M, (e_1 e_2)) \mapsto^{j+1} (M', e[v/x])$  and  $(k, \Psi, M) \sqsubseteq (k - j - 1, \lfloor \Psi' \rfloor_{k-j-1}, M')$ , as well as  $e[v/x] :_{k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M'} \tau_2$ . These three statements imply  $(e_1 e_2) :_{k, \Psi, M} \tau_2$ .  $\square$

**Theorem 33 (Application)**

If  $\Gamma$  is a context,  $e_1$  and  $e_2$  are (possibly open) terms, and  $\tau_1$  and  $\tau_2$  are types such that  $\Gamma \models e_1 : \tau_1 \rightarrow \tau_2$  and  $\Gamma \models e_2 : \tau_1$  then  $\Gamma \models (e_1 e_2) : \tau_2$

**Proof:** We must prove that under the premises of the theorem and for any  $k \geq 0$  we have  $\Gamma \models_k (e_1 e_2) : \tau_2$ . More specifically, for any  $M, \Psi$ , and  $\sigma$  such that  $M :_k \Psi$  and  $\sigma :_{k, \Psi, M} \Gamma$  we must show  $\sigma(e_1 e_2) :_{k, \Psi, M} \tau_2$ . By the premises of the theorem we have  $\sigma(e_1) :_{k, \Psi, M} \tau_1 \rightarrow \tau_2$  and  $\sigma(e_2) :_{k, \Psi, M} \tau_1$ . The result now follows from Lemma 32.  $\square$

**Theorem 34 (Abstraction)**

Let  $\Gamma$  be a context, let  $\tau_1$  and  $\tau_2$  be types, and let  $\Gamma[x := \tau_1]$  be the context that is identical to  $\Gamma$  except that it maps  $x$  to  $\tau_1$ . If  $\Gamma[x := \tau_1] \models e : \tau_2$  then  $\Gamma \models \lambda x.e : \tau_1 \rightarrow \tau_2$ .

**Proof:** As in theorem 33 we must show under the premises of the theorem that for any  $k \geq 0$  and  $M, \Psi$  and  $\sigma$  such that  $M :_k \Psi$  and  $\sigma :_{k, \Psi, M} \Gamma$  we have  $\sigma(\lambda x.e) :_{k, \Psi, M} \tau_1 \rightarrow \tau_2$ . Suppose  $M :_k \Psi$  and  $\sigma :_{k, \Psi, M} \Gamma$ . Let  $j < k$  and  $v, \Psi'$ , and  $M'$  be such that  $(k, \Psi, M) \sqsubseteq (j, \Psi', M')$  and  $\langle j, \Psi', M', v \rangle \in \tau_1$ . By definition of  $\rightarrow$  it now suffices to show that  $\sigma(e[v/x]) :_{j, \Psi', M'} \tau_2$ . Let  $\sigma[x := v]$  be the substitution identical to  $\sigma$  except that it maps  $x$  to  $v$ . Since the codomain of  $\Gamma$  contains types (which are closed under memory extension), and since  $v :_{j, \Psi', M'} \tau_1$ , we now have that  $\sigma[x := v] :_{j, \Psi', M'} \Gamma[x := \tau_1]$ . By the last premise of the theorem it follows that  $\sigma[x := v](e) :_{j, \Psi', M'} \tau_2$ . But this implies  $\sigma(e[v/x]) :_{j, \Psi', M'} \tau_2$ .  $\square$

### Definition 35 (Extend Memory Typing)

$$\text{extmtype}(k, \Psi, \ell, \tau) = (\ell \mapsto \lfloor \tau \rfloor_k) \cup \lfloor \Psi \rfloor_k$$

### Lemma 36

If  $\tau$  is a type such that  $\langle k, \Psi, M, v \rangle \in \tau$  where  $M :_k \Psi$ ,  $\ell \notin \text{dom}(\Psi)$  and  $\ell \notin \text{dom}(M)$ , then

1.  $(k, \Psi, M) \sqsubseteq (k, \text{extmtype}(k, \Psi, \ell, \tau), M[\ell := v])$
2.  $M[\ell := v] :_k \text{extmtype}(k, \Psi, \ell, \tau)$

**Proof:** We prove (1) and (2) simultaneously by induction on  $k$ .

For (1) we must show the following:

- (a)  $k \leq k$ . Immediate.
- (b)  $\forall \ell' \in \text{dom}(\Psi)$ .  
 $\lfloor \Psi \rfloor_k(\ell') = \lfloor \text{extmtype}(k, \Psi, \ell, \tau) \rfloor_k(\ell')$ .  
 Trivial by observing that since  $\ell' \neq \ell$ , by the definition of  $\text{extmtype}$ ,  $\lfloor \text{extmtype}(k, \Psi, \ell, \tau) \rfloor_k(\ell') = \lfloor \Psi \rfloor_k(\ell')$ .
- (c)  $M[\ell := v] :_k \text{extmtype}(k, \Psi, \ell, \tau)$ . From (2).

For (2) we must show the following:

- (a)  $\text{dom}(\text{extmtype}(k, \Psi, \ell, \tau)) \subseteq \text{dom}(M[\ell := v])$ .  
Follows easily from premises.
- (b)  $\text{finite}(\text{dom}(M[\ell := v]))$ .  
Follows easily from premises.
- (c)  $\forall j < k$ .  $\forall \ell' \in \text{dom}(\text{extmtype}(k, \Psi, \ell, \tau))$ .  
 $\langle j, \lfloor \text{extmtype}(k, \Psi, \ell, \tau) \rfloor_j, M[\ell := v], M[\ell := v](\ell') \rangle$   
 $\in \lfloor \text{extmtype}(k, \Psi, \ell, \tau) \rfloor_k(\ell')$ .

Case  $\ell' \neq \ell$ :

- $\langle j, \lfloor \Psi \rfloor_j, M, M(\ell') \rangle \in \lfloor \Psi \rfloor_k(\ell')$  by  $M :_k \Psi$ .
- By IH (1), since  $j < k$ ,  $(j, \lfloor \Psi \rfloor_j, M) \sqsubseteq (j, \lfloor \text{extmtype}(j, \lfloor \Psi \rfloor_j, \ell, \tau) \rfloor_j, M[\ell := v])$ .
- Since  $\text{type}(\Psi(\ell'))$   
 $\langle j, \lfloor \text{extmtype}(j, \lfloor \Psi \rfloor_j, \ell, \tau) \rfloor_j, M[\ell := v], M(\ell') \rangle \in \lfloor \Psi \rfloor_k(\ell')$ . But by definition of  $\text{extmtype}$  we have  $\lfloor \Psi \rfloor_k(\ell') = \lfloor \text{extmtype}(k, \Psi, \ell, \tau) \rfloor_k(\ell')$ .

Case  $\ell' = \ell$ :

- $M[\ell := v](\ell') = v$ .
- Since  $\langle k, \Psi, M, v \rangle \in \tau$  and  $j < k$ , we have  $\langle j, \lfloor \Psi \rfloor_j, M, v \rangle \in \lfloor \tau \rfloor_k$ .
- The rest of the proof is similar to case  $\ell' \neq \ell$ ; we note that  $\lfloor \text{extmtype}(k, \Psi, \ell, \tau) \rfloor_k(\ell') = \lfloor \tau \rfloor_k$ .

□

### Lemma 37 (Closed New)

If  $e$  is a closed term and  $\tau$  is a type set such that  $e :_{k, \Psi, M} \tau$  then  $\text{new}(e) :_{k, \Psi, M} \text{ref } \tau$ .

**Proof:** Since  $\tau$  is a type by Lemma 11 we have that  $\text{ref } \tau$  is a type. Then it follows from  $e :_{k, \Psi, M} \tau$  that  $(M, e)$  is safe for  $k$  steps. Hence, if the state  $(M, \text{new}(e))$  reduces for  $k$  steps without reaching a state of the form  $(M', \text{new}(v))$  then it follows that  $\text{new}(e) :_{k, \Psi, M} \text{ref } \tau$ . So we assume, without loss of generality, that  $(M, \text{new}(e)) \mapsto^j (M', \text{new}(v))$  for some  $j < k$ , memory  $M'$ , and value  $v$ . Since  $e :_{k, \Psi, M} \tau$ , definition 5 (Expr : Type) implies that there exists a  $\Psi'$  such that we have (1) and (2) below.

1.  $(k, \Psi, M) \sqsubseteq (k - j, \Psi', M')$
2.  $\langle k - j, \Psi', M', v \rangle \in \tau$
3. From (1) and definitions 3 and 2 (Memory Extension and Well-typed Memory) we have that  $M'$  is finite. It follows that there exists some location  $\ell \notin \text{dom}(M')$ . Hence, we may assume that  $(M', \text{new}(v)) \mapsto (M'[\ell := v], \ell)$ .
4. From (2) it follows that  $M' :_{k-j} \Psi'$  (since  $\text{type}(\tau)$ ).
5. Pick  $\Psi'' = \text{extmtype}(k - j - 1, \lfloor \Psi' \rfloor_{k-j-1}, \ell, \tau)$ . Note that  $\lfloor \Psi'' \rfloor_{k-j-1} = \Psi''$  by the definition of  $\text{extmtype}$ .
6. The following information-forgetting memory extension holds:  $(k - j, \Psi', M') \sqsubseteq (k - j - 1, \lfloor \Psi' \rfloor_{k-j-1}, M')$ .
7. From (2) and (6) and the premise that  $\tau$  is a type, we have  $\langle k - j - 1, \lfloor \Psi' \rfloor_{k-j-1}, M', v \rangle \in \tau$ . From (4) it follows that  $M' :_{k-j-1} \lfloor \Psi' \rfloor_{k-j-1}$  and from (3) we have  $\ell \notin \text{dom}(M')$  and  $\ell \notin \text{dom}(\lfloor \Psi' \rfloor_{k-j-1})$ . Finally, from (5) we have  $\Psi'' = \text{extmtype}(k - j - 1, \Psi', \ell, \tau)$ . Hence, we may use Lemma 36 to conclude the following:
  - (a)  $(k - j - 1, \lfloor \Psi' \rfloor_{k-j-1}, M') \sqsubseteq (k - j - 1, \Psi'', M'[\ell := v])$ .
  - (b)  $M'[\ell := v] :_{k-j-1} \Psi''$ .
8. We must show that  $\lfloor \Psi'' \rfloor_{k-j-1}(\ell) = \lfloor \tau \rfloor_{k-j-1}$ . But  $\Psi'' = \text{extmtype}(k - j - 1, \Psi', \ell, \tau)$  and the definition of  $\text{extmtype}$  implies  $\lfloor \Psi'' \rfloor_{k-j-1} = \Psi''$ . Hence, we must show  $\Psi''(\ell) = \lfloor \tau \rfloor_{k-j-1}$  which is immediate from the definition of  $\text{extmtype}$ .
9. Let  $i < (k - j - 1)$ . We must show that  $\langle i, \lfloor \Psi'' \rfloor_i, M'[\ell := v], M'[\ell := v](\ell) \rangle \in \tau$ . By definition 35 ( $\text{extmtype}$ ),  $\ell \in \text{dom}(\Psi'')$ . Then, from (7b) we can conclude that  $\langle i, \lfloor \Psi'' \rfloor_i, M'[\ell := v], M'[\ell := v](\ell) \rangle \in \lfloor \Psi'' \rfloor_{k-j-1}(\ell)$ . Then, since  $\lfloor \Psi'' \rfloor_{k-j-1}(\ell) = \lfloor \tau \rfloor_{k-j-1}$  (as we proved in step (8)), and since  $i < (k - j - 1)$ , it follows that  $\langle i, \lfloor \Psi'' \rfloor_i, M'[\ell := v], M'[\ell := v](\ell) \rangle \in \tau$ .
10. From (7), (8), and (9) it follows that  $\langle k - j - 1, \Psi'', M'[\ell := v], \ell \rangle \in \text{ref } \tau$ .

Finally, we have the following:

- $(M, \text{new}(e)) \mapsto^{j+1} (M'[\ell := v], \ell)$  (from  $(M, \text{new}(e)) \mapsto^j (M', \text{new}(v))$  and (3));

- $(k, \Psi, M) \sqsubseteq (k-j-1, \Psi'', M' [\ell := v])$  (from (1), (6), (7a), and transitivity of  $\sqsubseteq$ );
- $\langle k-j-1, \Psi'', M' [\ell := v], \ell \rangle \in \mathbf{ref} \tau$  (from (10)).

These three statements imply that  $\mathbf{new}(e) :_{k, \Psi, M} \mathbf{ref} \tau$ .  $\square$

The type inference theorem for expressions of the form  $\mathbf{new}(e)$  now follows from Lemma 37 in the same manner that Theorem 33 follows from Lemma 32.

### Lemma 38 (Closed Dereferencing)

If  $e$  is a closed term and  $\tau$  is a type set such that  $e :_{k, \Psi, M} \mathbf{ref} \tau$  then  $!e :_{k, \Psi, M} \tau$ .

**Proof:** Since  $\tau$  is a type by Lemma 11 we have that  $\mathbf{ref} \tau$  is a type. Then it follows from  $e :_{k, \Psi, M} \mathbf{ref} \tau$  that  $(M, e)$  is safe for  $k$  steps. Hence, the state  $(M, !e)$  either reduces for  $k$  steps without reaching a state of the form  $(M', !v)$ , in which case  $!e :_{k, \Psi, M} \tau$  (for any  $\tau$ ), or  $(M, !e) \mapsto^j (M', !\ell)$  where  $\ell \in \mathit{dom}(M')$  and  $j < k$ . In the latter case, since  $e :_{k, \Psi, M} \mathbf{ref} \tau$ , definition 5 (Expr : Type) implies that  $(k, \Psi, M) \sqsubseteq (k-j, \Psi', M')$  and  $\langle k-j, \Psi', M', \ell \rangle \in \mathbf{ref} \tau$ . By the definition of  $\mathbf{ref}$  it follows that  $\langle k-j-1, [\Psi']_{k-j-1}, M', M'(\ell) \rangle \in \tau$ . Also, since  $\tau$  is a type,  $\langle k-j-1, [\Psi']_{k-j-1}, M', M'(\ell) \rangle \in \tau$  implies that  $M' :_{k-j-1} [\Psi']_{k-j-1}$ . Then the following memory extension (where we forget some information) holds:  $(k-j, \Psi', M') \sqsubseteq (k-j-1, [\Psi']_{k-j-1}, M')$ . By Lemma 9 ( $\sqsubseteq$  transitive) it follows that  $(k, \Psi, M) \sqsubseteq (k-j-1, [\Psi']_{k-j-1}, M')$ . But we also have  $(M, !e) \mapsto^{j+1} (M', M'(\ell))$  (since  $(M, !v) \mapsto (M', M'(\ell))$ ), and  $\langle k-j-1, [\Psi']_{k-j-1}, M', M'(\ell) \rangle \in \tau$ . These three statements imply  $!e :_{k, \Psi, M} \tau$ .  $\square$

The type inference theorem for expressions of the form  $!e$  now follows from Lemma 38 in the same manner that Theorem 33 follows from Lemma 32.

### Lemma 39

If  $\tau$  is a type such that  $\langle k, \Psi, M, v \rangle \in \tau$  where  $M :_k \Psi$ ,  $\ell \in \mathit{dom}(\Psi)$  and  $\ell \in \mathit{dom}(M)$ , then

1.  $(k, \Psi, M) \sqsubseteq (k, \Psi, M [\ell := v])$
2.  $M [\ell := v] :_k \Psi$

**Proof:** Simultaneously by induction on  $k$ . The proof is similar to that of Lemma 36.  $\square$

### Lemma 40 (Closed Assignment)

If  $e_1$  and  $e_2$  are closed terms and  $\tau$  is a type set such that  $e_1 :_{k, \Psi, M} \mathbf{ref} \tau$  and  $e_2 :_{k, \Psi, M} \tau$  then  $e_1 := e_2 :_{k, \Psi, M} \mathbf{unit}$ .

**Proof:** Since  $\tau$  is a type, by Lemma 11  $\mathbf{ref} \tau$  is a type. Then, it follows from  $e_1 :_{k, \Psi, M} \mathbf{ref} \tau$  and  $e_2 :_{k, \Psi, M} \tau$  that  $(M, e_1)$  and  $(M, e_2)$  are both safe for  $k$  steps and that if  $(M, e_1)$  reduces to some  $(M', v_1)$  in fewer than  $k$  steps, the value  $v_1$  must be a location. Hence, the state  $(M, e_1 := e_2)$  either reduces for  $k$  steps without reaching a state of the form  $(M', v_1 := v_2)$  where  $v_1$  and  $v_2$  are values, or there exists a location  $\ell$  and a memory  $M'$  such that  $\ell \in \mathit{dom}(M')$ ,  $j < k$ , and  $(M, e_1 := e_2) \mapsto^j (M', \ell := v) \mapsto (M' [\ell := v], \mathbf{0})$ . In the first case we have that  $(M, e_1 := e_2)$  is safe for  $k$  steps and does not produce a value in less than  $k$  steps and hence  $e_1 := e_2 :_{k, \Psi, M} \mathbf{unit}$ . In the second case,  $e_1 :_{k, \Psi, M} \mathbf{ref} \tau$  and  $e_2 :_{k, \Psi, M} \tau$  imply that  $(k, \Psi, M) \sqsubseteq (k-j, \Psi', M')$  and  $\langle k-j, \Psi', M', \ell \rangle \in \mathbf{ref} \tau$  and  $\langle k-j, \Psi', M', v \rangle \in \tau$ .

We will now show  $(k-j, \Psi', M') \sqsubseteq (k-j-1, [\Psi']_{k-j-1}, M' [\ell := v])$ .

1. The following information-forgetting memory extension holds:  $(k-j, \Psi', M') \sqsubseteq (k-j-1, [\Psi']_{k-j-1}, M')$ .
2. Since  $\tau$  is a type, from  $\langle k-j, \Psi', M', v \rangle \in \tau$  and (1) it follows that  $\langle k-j-1, [\Psi']_{k-j-1}, M', v \rangle \in \tau$ .
3. From (2) and  $\mathit{type}(\tau)$  we have  $M' :_{k-j-1} [\Psi']_{k-j-1}$ .
4. From (2), (3),  $\mathit{type}(\tau)$ ,  $\ell \in \mathit{dom}(M')$ , and  $\ell \in \mathit{dom}([\Psi']_{k-j-1})$ , by Lemma 39 we may conclude the following:

- (a)  $(k-j-1, [\Psi']_{k-j-1}, M') \sqsubseteq (k-j-1, [\Psi']_{k-j-1}, M' [\ell := v])$ .
- (b)  $M' [\ell := v] :_{k-j-1} [\Psi']_{k-j-1}$ .

Finally we have the following:

- $(k, \Psi, M) \sqsubseteq (k-j-1, [\Psi']_{k-j-1}, M' [\ell := v])$  (by Lemma 9);
- $(M, e_1 := e_2) \mapsto^{j+1} (M' [\ell := v], \mathbf{0})$ ;
- $\langle k-j-1, [\Psi']_{k-j-1}, M' [\ell := v], \mathbf{0} \rangle \in \mathbf{unit}$ .

These three statements imply  $e_1 := e_2 :_{k, \Psi, M} \mathbf{unit}$ .  $\square$

The type inference theorem for expressions of the form  $e_1 := e_2$  now follows from Lemma 40 in the same manner that Theorem 33 follows from Lemma 32.

### Theorem 16 (Type Abstraction)

Let  $\Gamma$  be a context and let  $F$  be a nonexpansive type functional. If  $\Gamma \models e : F(\tau)$  for any type set  $\tau$ , then  $\Gamma \models \Lambda.e : \forall F$ .

**Proof:** Given in Section 4.  $\square$

### Theorem 14 (Type Application)

Let  $\Gamma$  be a context and let  $F$  be a function from types to types. If  $\Gamma \models e : \forall F$  then, for all  $\tau$  such that  $\tau$  is a type set,  $\Gamma \models e[] : F(\tau)$ .

**Proof:** Given in Section 4.  $\square$

**Lemma 41 (Closed Pack)**

If  $e$  is a closed term,  $\tau$  is a type set, and  $F$  is a function from types to types such that  $e :_{k,\Psi,M} F(\tau)$  then  $\text{pack } e :_{k,\Psi,M} \exists F$ .

**Proof:** Since  $\tau$  is a type and  $F$  is a function from types to types it follows that  $F(\tau)$  is a type. Then since  $e :_{k,\Psi,M} F(\tau)$  we have that  $(M, e)$  is safe for  $k$  steps. If the state  $(M, \text{pack } e)$  reduces for  $k$  steps without reaching a state of the form  $(M', \text{pack } v)$ , then we may conclude that  $\text{pack } e :_{k,\Psi,M} \exists F$ . Hence, we may assume without loss of generality that  $(M, \text{pack } e) \mapsto^j (M', \text{pack } v)$  where  $j < k$  and  $v$  is a value. Since  $e :_{k,\Psi,M} F(\tau)$ , by definition 5 (Expr : Type) there exists a memory typing  $\Psi'$  such that  $(k, \Psi, M) \sqsubseteq (k-j, \Psi', M')$  and  $\langle k-j, \Psi', M', v \rangle \in F(\tau)$ .

We now show that  $\langle k-j, \Psi', M', \text{pack } v \rangle \in \exists F$ . The proof is in three parts. First, from  $(k, \Psi, M) \sqsubseteq (k-j, \Psi', M')$  we immediately have that  $M' :_{k-j} \Psi'$ . Second, from  $\text{type}(\tau)$  (which is a premise of the lemma) there exists a set  $\tau$  such that  $\text{type}(\lfloor \tau \rfloor_{k-j})$ . Third, suppose  $i < (k-j)$ . We must show that  $\langle i, \lfloor \Psi' \rfloor_i, M', v \rangle \in F(\tau)$ . Note that the following information-forgetting memory extension holds:  $(k-j, \Psi', M') \sqsubseteq (i, \lfloor \Psi' \rfloor_i, M')$ . Since we already have that  $\langle k-j, \Psi', M', v \rangle \in F(\tau)$  and that  $F(\tau)$  is a type, we can conclude that  $\langle i, \Psi', M', v \rangle \in F(\tau)$ . Hence we have shown  $\langle k-j, \Psi', M', \text{pack } v \rangle \in \exists F$ .

Finally, we have  $(M, \text{pack } e) \mapsto^j (M', \text{pack } v)$ ,  $(k, \Psi, M) \sqsubseteq (k-j, \Psi', M')$ , and  $\langle k-j, \Psi', M', \text{pack } v \rangle \in \exists F$ . These three statements imply that  $\text{pack } e :_{k,\Psi,M} \exists F$ .  $\square$

The type inference theorem for  $\text{pack}$  now follows from Lemma 41 in the same manner that Theorem 33 follows from Lemma 32.

**Theorem 42 (Open)**

Let  $\Gamma$  be a context and let  $F$  be a nonexpansive type functional. If  $e_1$  and  $e_2$  are (possibly open) terms such that  $\Gamma \models e_1 : \exists F$  and  $\Gamma[x := F(\tau)] \models e_2 : \tau_2$  for any type set  $\tau$ , then  $\Gamma \models \text{open } e_1 \text{ as } x \text{ in } e_2 : \tau_2$ .

**Proof:** We must prove under the premises of the theorem for any  $k \geq 0$  and  $M, \Psi$ , and  $\sigma$  such that  $M :_k \Psi$  and  $\sigma :_{k,\Psi,M} \Gamma$ , that we have  $\sigma(\text{open } e_1 \text{ as } x \text{ in } e_2) :_{k,\Psi,M} \tau_2$ .

Suppose  $M :_k \Psi$  and  $\sigma :_{k,\Psi,M} \Gamma$ . By the premises of the theorem we have  $\sigma(e_1) :_{k,\Psi,M} \exists F$ . Now, since  $F$  is a nonexpansive functional by Lemma 31 we have that  $\exists F$  is a type. It follows that the state  $(M, \sigma(e_1))$  is safe for  $k$  steps and that if  $(M, \sigma(e_1))$  generates a value in fewer than  $k$  steps, that value must be of the form  $\text{pack } v$  where  $v$  is a value. Hence, if the state  $(M, \sigma(\text{open } e_1 \text{ as } x \text{ in } e_2))$  does not reduce to a state of the form  $(M, \sigma(\text{open } (\text{pack } v) \text{ as } x \text{ in } e_2))$  in fewer than  $k$  steps, then we immediately have

$\sigma(\text{open } e_1 \text{ as } x \text{ in } e_2) :_{k,\Psi,M} \tau_2$ . So without loss of generality, we can assume that  $(M, \sigma(\text{open } e_1 \text{ as } x \text{ in } e_2)) \mapsto^j (M', \sigma(\text{open } (\text{pack } v) \text{ as } x \text{ in } e_2))$  for  $j < k$  and memory  $M'$ . Then the operational semantics in Figure 1 allows us to conclude that  $(M, \sigma(e_1)) \mapsto^j (M', \sigma(\text{pack } v))$  (where  $\sigma(\text{pack } v) = \text{pack } v$  since a value  $v$  has no free variables). Since  $\sigma(e_1) :_{k,\Psi,M} \exists F$ , by definition 5 (Expr : Type) there exists a memory typing  $\Psi'$  such that  $(k, \Psi, M) \sqsubseteq (k-j, \Psi', M')$  and  $\langle k-j, \Psi', M', \text{pack } v \rangle \in \exists F$ . Then by the definition of  $\exists F$  we have that there exists a set  $\tau$  such that  $\lfloor \tau \rfloor_{k-j}$  is a type and  $\langle k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M', v \rangle \in F(\tau)$ .

By the premises of the theorem, since  $\lfloor \tau \rfloor_{k-j}$  is a type, we have  $\Gamma[x := F(\lfloor \tau \rfloor_{k-j})] \models e_2 : \tau_2$ . To make further use of this statement, we will first have to show that  $\langle k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M', v \rangle \in F(\lfloor \tau \rfloor_{k-j})$ . From  $\langle k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M', v \rangle \in F(\tau)$  and definition 1 (Approx) we can conclude that  $\langle k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M', v \rangle \in \lfloor F(\tau) \rfloor_{k-j}$  (since  $(k-j-1) < (k-j)$ ). By the premises of the theorem  $F$  is nonexpansive, which implies that  $\lfloor F(\tau) \rfloor_{k-j} = \lfloor F(\lfloor \tau \rfloor_{k-j}) \rfloor_{k-j}$ . So we have  $\langle k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M', v \rangle \in \lfloor F(\lfloor \tau \rfloor_{k-j}) \rfloor_{k-j}$ . But this implies (since  $(k-j-1) < (k-j)$ ) that  $\langle k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M', v \rangle \in F(\lfloor \tau \rfloor_{k-j})$  as we wanted to show.

Let  $\sigma[x := v]$  be the substitution that is identical to  $\sigma$  except that it maps  $x$  to  $v$ . Recall that the codomain of  $\Gamma$  contains types (which are closed under memory extension). Note that the following information-forgetting memory extension holds:  $(k-j, \Psi', M') \sqsubseteq (k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M')$ . Then by Lemma 9 ( $\sqsubseteq$  transitive) we have  $(k, \Psi, M) \sqsubseteq (k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M')$ . This, together with  $\langle k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M', v \rangle \in F(\lfloor \tau \rfloor_{k-j})$  (which we proved above), allows us to conclude that  $\sigma[x := v] :_{k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M'} \Gamma[x := F(\lfloor \tau \rfloor_{k-j})]$ . Hence from  $\Gamma[x := F(\lfloor \tau \rfloor_{k-j})] \models e_2 : \tau_2$  it follows that  $\sigma[x := v](e_2) :_{k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M'} \tau_2$ . But this implies  $\sigma(e_2[v/x]) :_{k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M'} \tau_2$ .

Finally, we have  $(M, \sigma(\text{open } e_1 \text{ as } x \text{ in } e_2)) \mapsto^{j+1} (M', \sigma(e_2[v/x])); (k, \Psi, M) \sqsubseteq (k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M')$ ; and  $\sigma(e_2[v/x]) :_{k-j-1, \lfloor \Psi' \rfloor_{k-j-1}, M'} \tau_2$ . Hence, we can conclude that  $\sigma(\text{open } e_1 \text{ as } x \text{ in } e_2) :_{k,\Psi,M} \tau_2$ .  $\square$